

A Logic for Information Flow Analysis with an Application to Forward Slicing of Simple Imperative Programs

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Abstract

We specify an information flow analysis for a simple imperative language, using a Hoare-like logic. The logic facilitates static checking of a larger class of programs than can be checked by extant type-based approaches in which a program is deemed insecure when it contains an insecure subprogram. The logic is based on an abstract interpretation of program traces that makes independence between program variables explicit. Unlike other, more precise, approaches based on Hoare logics, our approach does not require a theorem prover to generate invariants. We demonstrate the modularity of our approach by showing that a frame rule holds in our logic. Finally, we show how our logic can be applied to a program transformation, namely, forward slicing: given a derivation of a program in the logic, with the information that variable l is independent of variable h , the slicing transformation systematically creates the forward l -slice of the program: the slice contains all the commands independent of h . We show that the slicing transformation is semantics preserving.

Key words: Abstract interpretation, denotational semantics, frame rule, Hoare logic, information flow analysis, program slicing, strongest postcondition.

1 Introduction

This paper specifies an information flow analysis using a Hoare-like logic and considers an application of the logic to forward slicing in simple imperative

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programs.

Given a system with high, or secret (H), and low, or public (L) inputs and outputs, where $L \leq H$ is a security lattice, a classic security problem is how to enforce the following end-to-end *confidentiality* policy: protect secret data, i.e., prevent leaks of secrets at public output channels. An information flow analysis checks if a program satisfies the policy. Denning and Denning were the first to formulate an information flow analysis for confidentiality[13]. Subsequent advances have been comprehensively summarized in the recent survey by Sabelfeld and Myers [28]. An oft-used approach for specifying static analyses for information flow is *security type systems* [24,31]. Security types can be assigned to program variables and expressions annotated with security levels. Security typing rules prevent leaks of secret information to public channels. For example, the security typing rule for assignment prevents H data from being assigned to a L variable. A well-typed program “protects secrets”, i.e., no information flows from H to L during program execution.

In the security literature, “protects secrets” is formalized as *noninterference* [15] and is described in terms of an “indistinguishability” relation on states. Two program states are indistinguishable for L if they agree on values of L variables. The noninterference property says that any two runs of a program starting from two initial states that are indistinguishable for L, yield two final states that are indistinguishable for L. The two initial states may differ on values of H variables but not on values of L variables; the two final states must agree on the current values of L variables. One reading of the noninterference property is as a form of independence [9]: L output is independent of H inputs. It is this notion of independence that is made explicit in the information flow analysis specified in this paper.

A shortcoming of usual type-based approaches for information flow [5,16,31,25] is that a type system can be too imprecise. Consider the sequential program $l := h ; l := 0$, where l has type L and h has type H. Although a programmer would never write such a program, it may arise naturally as a result of program transformation. The program is rejected by a security type system on account of the first assignment. But the program obviously satisfies noninterference – final states of any two runs of the program will always have the same value, 0, for l and are thus indistinguishable for L. Similarly, the program $h := l ; l := h$ is secure, yet is rejected by a security type system.

How can we admit such programs? Our inspiration comes from abstract interpretation [10], which can be viewed as a method for statically computing approximations of program invariants [11]. A benefit of this view is that the static abstraction of a program invariant can be used to annotate a program with pre- and postconditions and the annotated program can be checked against a Hoare-like logic. In information flow analysis, the invariant of inter-

est is *independence*, for which we use the notation $[x \times w]$ to denote that x is independent of variable w . The intuition is this: a command denotes a *trace transformer* (formalized in Section 2) that transforms a pre-trace to a post-trace. A trace, in turn, is a store transformer, that transforms an initial store to either a current store or \perp where \perp denotes nontermination. If x is a variable, then $[x \times w]$ holds for a trace T provided for any two initial stores that differ only on the value of w , if T transforms them into current stores that are non- \perp then the current stores agree on the value of x . Alternatively, if x is \perp , then $[\perp \times w]$ holds for T provided for any two initial stores that differ only on the value of w , if T transforms them into two current stores then the one store is \perp if and only if the other store is. The intuition above is just a convenient restatement of noninterference but we tie it to the static notion of independence.

Our approach statically computes finite abstractions of the concrete traces before and after the execution of a command. The notation $T^\#$ will be used to describe an abstraction of a concrete trace T . This is formalized in Section 3. We formulate (in Section 4) a Hoare-like logic for checking independences and show (Section 5) that a checked program satisfies noninterference. The assertion language of the logic is decidable since it is just the language of finite sets of independences with subset inclusion. Specifications in the logic have the form $\{T_0^\#\} C \{T^\#\}$. Given precondition $T^\#$, we show in Section 7 how to compute strongest postconditions; for programs with loops, this necessitates a fixpoint computation³. We show that the logic deems the program $l := h; l := 0$ secure: the strongest postcondition of the program contains the independence $[l \times h]$.

Our approach falls in between type-based analysis and full verification where verification conditions for loops depend on loop invariants generated by a theorem prover. Instead, we approximate invariants using a fixpoint computation. Our approach is modular and we show that our logic satisfies a frame rule (Section 8). The frame rule permits local reasoning about a program: the relevant independences for a program are only those $[x \times w]$ where x occurs in the program. Moreover, in a larger context, the frame rule allows the following inference (in analogy with [22]): start with a specification $\{T_0^\#\} C \{T^\#\}$ describing independences before and after store modifications; then, $\{T_0^\# \cup T_1^\#\} C \{T^\# \cup T_1^\#\}$ holds provided C does not modify any variable y where $[y \times w]$ appears in $T_1^\#$. The initial specification $\{T_0^\#\} C \{T^\#\}$, can reason with only the slice of store that C touches.

³ The set of independences is a finite lattice, hence the fixpoint computation will terminate.

Contributions. To summarize, this paper makes three contributions. First and foremost, we formulate information flow analysis in a logical form via a Hoare-like logic. The approach deems more programs secure than extant type-based approaches. In Section 9, we show how our logic conservatively extends the security type system of Smith and Volpano [31], by showing that any well-typed program in their system satisfies the invariant $[\mathbf{l} \times \mathbf{h}]$. Secondly, we describe the relationship between information flow and program dependence, explored in [1,17], in a more direct manner by computing independences between program variables. The independences themselves are static descriptions of the noninterference property. The development in this paper considers *non-termination sensitive* noninterference: we assume that an attacker can observe nontermination. Finally, in Section 6, we show an application of our logic to forward slicing. Given a derivation of a program in the logic, with the information that variable \mathbf{l} is independent of variable \mathbf{h} , the slicing transformation systematically creates the forward \mathbf{l} -slice of the program: the slice contains all the commands independent of \mathbf{h} . We show that the slicing transformation is semantics preserving.

Differences from the conference version. Apart from the removal of some infelicities of notation, we have made three additional contributions not present in the conference version of this paper [2].

- We consider nontermination sensitive noninterference here, compared to nontermination insensitive noninterference in the conference version. For that purpose, we needed to add a new kind of independence, which in turn necessitated changes in the logic – specifically, in the rule for loops.
- To prove the resulting logic semantically correct, the semantics had to be modified, since the one given in [2] had no explicit notion of non-termination. As an extra benefit, the resulting denotational semantics is significantly simpler than the previous one.
- We show an application of the logic to forward slicing. Although the connection between information flow analysis and slicing was explored by Abadi et. al. [1], that paper did not provide a means to compute forward slices, which we present here.

On the other hand, the conference version of this paper contained a section on counterexample generation, which we have chosen to omit in this paper. We feel that the results might merit a separate paper after some strengthening.

2 Language: Syntax, Traces, Semantics

This section gives the syntax of a simple imperative language, formalizes the notion of traces, and gives the semantics of the language in terms of traces.

Syntax. We consider a simple imperative language with assignment, sequencing, conditionals and loops as formalized by the following BNF. Commands $C \in \mathbf{Cmd}$ are given by the syntax

$$C ::= x := E \mid C_1 ; C_2 \mid \mathbf{if} E \mathbf{then} C_1 \mathbf{else} C_2 \mid \mathbf{while} E \mathbf{do} C$$

where \mathbf{Var} is an infinite set of variables, $x, y, z, w \in \mathbf{Var}$ range over variables and where $E \in \mathbf{Exp}$ ranges over expressions. Expressions are left unspecified but we shall assume the existence of a function $\text{fv}(E)$ that computes the free variables of expression E . For commands, $\text{fv}(C)$ is defined in the obvious way. We also define a function $\text{modified} : \mathbf{Cmd} \rightarrow \mathcal{P}(\mathbf{Var})$ that given a command, returns the set of variables potentially assigned to by the command.

$$\begin{aligned} \text{modified}(x := E) &= \{x\} \\ \text{modified}(C_1 ; C_2) &= \text{modified}(C_1) \cup \text{modified}(C_2) \\ \text{modified}(\mathbf{if} E \mathbf{then} C_1 \mathbf{else} C_2) &= \text{modified}(C_1) \cup \text{modified}(C_2) \\ \text{modified}(\mathbf{while} E \mathbf{do} C) &= \text{modified}(C) \end{aligned}$$

In our examples, we shall often use a **skip** command; the formal treatment of that command (wrt. the semantics and the logic) should be obvious and we shall not bother to write down the rules explicitly.

Stores. A store, $s \in \mathbf{Sto}$, associates each variable with its current value; here values $v \in \mathbf{Val}$ are yet unspecified but we assume that there exists a predicate true? on \mathbf{Val} . (For instance, we could have \mathbf{Val} as the set of integers and let $\text{true?}(v)$ be defined as $v \neq 0$). The following notation is used to denote a store update:

- $[s \mid \mathbf{y} \mapsto v]$ returns a store s' with the property: for all $x \in \mathbf{Var}$, if $x \neq \mathbf{y}$ then $s' x = s x$; but $s'(\mathbf{y}) = v$.

Stores $s_1, s_2 \in \mathbf{Sto}_\perp$ agree on $x \in \mathbf{Var} \cup \{\perp\}$, written, $s_1 \stackrel{x}{=} s_2$, when either of the following conditions hold: (a) s_1, s_2, x are all non- \perp and then $s_1 x = s_2 x$; (b) $x \neq \perp$ and either $s_1 = \perp$ or $s_2 = \perp$ (or both); (c) $x = \perp$ and then $s_1 = \perp$ if and only if $s_2 = \perp$. This is made precise in Fig. 1 and motivated shortly,

For partial correctness – i.e., when we do not care about nontermination – we define, for $x \neq \perp$:

$$s_1 \stackrel{x}{=} s_2 \iff ((s_1 \neq \perp \wedge s_2 \neq \perp) \Rightarrow s_1 x = s_2 x)$$

For total correctness – i.e., when we are interested in nontermination – we additionally define, for $x = \perp$:

$$s_1 \stackrel{x}{=} s_2 \iff (s_1 = \perp \iff s_2 = \perp)$$

Fig. 1. When are two stores equal on x ?

in Section 3. Note that for $s_1, s_2 \in \mathbf{Sto}$ and $x \in \mathbf{Var}$, $s_1 \stackrel{x}{=} s_2$ amounts to $s_1 x = s_2 x$.

We write $s_1 \stackrel{w}{=} s_2$, where $s_1, s_2 \in \mathbf{Sto}$ and $w \in \mathbf{Var}$, to denote that for all variables $y \neq w$, $s_1 y = s_2 y$ holds. That is, the values of all variables, *except of* w , are equal in stores s_1 and s_2 .

Traces. A trace, $T \in \mathbf{Trc}$, maps an initial store to either a current store or \perp , where \perp denotes nontermination. Thus $\mathbf{Trc} = \mathbf{Sto} \rightarrow \mathbf{Sto}_\perp$. Note that \mathbf{Sto}_\perp is a complete partial order (CPO) with the ordering \sqsubseteq defined as: $s_1 \sqsubseteq s_2$ iff either $s_1 = \perp$ or s_1 equals s_2 . Thus \mathbf{Trc} is a CPO, under the pointwise ordering: $T_1 \sqsubseteq T_2$ iff for all $s \in \mathbf{Sto}$, $T_1 s \sqsubseteq T_2 s$.

Semantics. For expressions, we assume there exists a semantic function $\llbracket E \rrbracket : \mathbf{Sto} \rightarrow \mathbf{Val}$ which satisfies the following property:

Property 2.1 *If for all $x \in \text{fv}(E)$ we have $s_1 x = s_2 x$, with $s_1, s_2 \in \mathbf{Sto}$, then $\llbracket E \rrbracket s_1 = \llbracket E \rrbracket s_2$.*

The definition of $\llbracket E \rrbracket$ would contain the clause $\llbracket x \rrbracket s = s x$. The semantics of a command has functionality $\llbracket C \rrbracket : \mathbf{Trc} \rightarrow \mathbf{Trc}$, and is defined in Fig. 2. Since \mathbf{Trc} is a CPO also $\mathbf{Trc} \rightarrow \mathbf{Trc}$ is a CPO, with the following pointwise ordering: $f_1 \sqsubseteq f_2$ iff $f_1(T) \sqsubseteq f_2(T)$ for all $T \in \mathbf{Trc}$.

Two comments regarding Fig. 2: First, to streamline the treatment of \perp , the metalanguage expression “let $\alpha = \beta$ in ...” denotes \perp if β is \perp .

$$\begin{aligned}
\llbracket x := E \rrbracket &= \lambda T. \lambda s. \text{let } s' = T s \text{ in } [s' \mid x \mapsto \llbracket E \rrbracket s'] \\
\llbracket C_1 ; C_2 \rrbracket &= \lambda T. \llbracket C_2 \rrbracket (\llbracket C_1 \rrbracket T) \\
\llbracket \text{if } E \text{ then } C_1 \text{ else } C_2 \rrbracket &= \lambda T. \lambda s. \text{let } s' = T s \text{ in} \\
&\quad \text{if } \text{true?}(\llbracket E \rrbracket s') \text{ then } \llbracket C_1 \rrbracket T s \text{ else } \llbracket C_2 \rrbracket T s \\
\llbracket \text{while } E \text{ do } C_0 \rrbracket &= \text{Ifp}(\mathcal{F}) \quad \text{where} \\
&\quad \mathcal{F} : (\mathbf{Trc} \rightarrow \mathbf{Trc}) \rightarrow (\mathbf{Trc} \rightarrow \mathbf{Trc}) \text{ is given by} \\
&\quad \mathcal{F}(f) = \lambda T. \lambda s. \text{let } s' = T s \text{ in} \\
&\quad \quad \text{if } \text{true?}(\llbracket E \rrbracket s') \text{ then } f(\llbracket C_0 \rrbracket T) s \text{ else } s'
\end{aligned}$$

Fig. 2. The Trace Semantics.

Second, in order to show that the least fixed point in the definition of **while** is well-defined, \mathcal{F} (called the *functor* of the **while** command) ought to be continuous. But this follows from the following calculation:

$$\begin{aligned}
\mathcal{F}(\sqcup_i f_i) &= \lambda T. \lambda s. \text{let } s' = T s \text{ in (if } \text{true?}(\llbracket E \rrbracket s') \text{ then } (\sqcup_i f_i)(\llbracket C_0 \rrbracket T) s \text{ else } s') \\
&= \lambda T. \lambda s. \text{let } s' = T s \text{ in (if } \text{true?}(\llbracket E \rrbracket s') \text{ then } \sqcup_i (f_i(\llbracket C_0 \rrbracket T) s) \text{ else } s') \\
&= \sqcup_i (\lambda T. \lambda s. \text{let } s' = T s \text{ in (if } \text{true?}(\llbracket E \rrbracket s') \text{ then } f_i(\llbracket C_0 \rrbracket T) s \text{ else } s')) \\
&= \sqcup_i (\mathcal{F}(f_i))
\end{aligned}$$

Therefore, with C of the form **while** E **do** C_0 ,

$$\llbracket C \rrbracket = \text{Ifp}(\mathcal{F}) = \bigsqcup_{i \in \mathcal{N}} f_i$$

where f_i (called an *iterand* of the **while** command) is defined by:

$$\begin{aligned}
f_0 &= \lambda T. \lambda s. \perp \\
f_{i+1} &= \mathcal{F}(f_i)
\end{aligned}$$

We say that a function $f \in \mathbf{Trc} \rightarrow \mathbf{Trc}$ is *fully strict* if for all $T \in \mathbf{Trc}$ and $s \in \mathbf{Sto}$, $T s = \perp$ implies $f T s = \perp$.

We say that a fully strict function f *preserves* $z \in \mathbf{Var}$ if whenever $f T s \neq \perp$ (and thus $T s \neq \perp$) then $f T s z = T s z$.

We now have the following results about the semantic functions:

Fact 2.2 *For all commands C , the function $\llbracket C \rrbracket$ is monotone and fully strict. Also, all iterands f_i of **while** commands are monotone and fully strict.*

PROOF. We go by structural induction on C . For **while**, we show that \mathcal{F} maps monotone functions into monotone functions and fully strict functions into fully strict functions; the result then follows (as f_0 is clearly monotone and fully strict) since the limit of monotone functions is itself monotone, and the limit of fully strict functions is itself fully strict.

Lemma 2.3 *For all commands C , and all $z \in \mathbf{Var}$ with $z \notin \text{modified}(C)$,*

- $\llbracket C \rrbracket$ preserves z ;
- if C is a **while** command then all its iterands f_i preserve z .

PROOF. Structural induction in C , with a case analysis. In all cases we are given T and s , and assume that $\llbracket C \rrbracket T s \neq \perp$ (and by Fact 2.2 thus $T s \neq \perp$); we must show that $\llbracket C \rrbracket T s z = T s z$.

$C = x := E$. From $z \notin \text{modified}(C)$ we infer that $z \neq x$, and the claim is trivial.

$C = C_1 ; C_2$. Since $z \notin \text{modified}(C_2)$, we infer inductively that $\llbracket C_2 \rrbracket (\llbracket C_1 \rrbracket T) s z = \llbracket C_1 \rrbracket T s z$. Since $z \notin \text{modified}(C_1)$, we infer inductively that $\llbracket C_1 \rrbracket T s z = T s z$. We thus get $\llbracket C \rrbracket T s z = \llbracket C_2 \rrbracket (\llbracket C_1 \rrbracket T) s z = T s z$, as desired.

$C = \text{if } E \text{ then } C_1 \text{ else } C_2$. Wlog. we can assume that $\text{true?}(\llbracket E \rrbracket(T s))$, in which case $\llbracket C \rrbracket T s z = \llbracket C_1 \rrbracket T s z$. Since $z \notin \text{modified}(C_1)$, we infer inductively that $\llbracket C_1 \rrbracket T s z = T s z$. This yields the claim.

$C = \text{while } E \text{ do } C_0$. Let f_i be the iterands of C ; our first task is to prove by induction in i that each f_i preserves z . For $i = 0$, the claim follows vacuously since $f_i T s = \perp$. For the inductive case, we split into two cases, in both cases assuming $f_{i+1} T s \neq \perp$ (and by Fact 2.2 thus $T s \neq \perp$):

- if $\text{true?}(\llbracket E \rrbracket(T s))$ then

$$f_{i+1} T s z = f_i(\llbracket C_0 \rrbracket T) s z = \llbracket C_0 \rrbracket T s z = T s z$$

where the second equality follows from the induction hypothesis on f_i , and the third equality follows from the overall induction hypothesis on C_0 (applicable since $z \notin \text{modified}(C_0)$).

- if $\text{true?}(\llbracket E \rrbracket(T s))$ does not hold, then $f_{i+1} T s z = T s z$ follows directly.

We are left with showing that also $\llbracket \mathbf{C} \rrbracket$, given as the least upper bound of the iterands, preserves z . But since $\llbracket \mathbf{C} \rrbracket \top s \neq \perp$, we infer that there exists a natural number j such that $\llbracket \mathbf{C} \rrbracket \top s = f_j \top s$, yielding the desired $\llbracket \mathbf{C} \rrbracket \top s z = f_j \top s z = \top s z$.

3 Independences

The trace semantics says that the meaning of a command is a trace transformer: it transforms initial (or pre) traces into current (or post) traces. In this section we will be interested in a finite abstraction of the pre-traces and the post-traces relevant to the execution of a command. The abstract traces are termed *independences*: an independence $\top^\# \in \mathbf{Independ} = \mathcal{P}((\mathbf{Var} \cup \{\perp\}) \times \mathbf{Var})$ is a set of pairs of the form $[x \times w]$. If x is a variable, then $[x \times w]$ denotes that the *current* value of x is independent of the *initial* value of w . If x is \perp , then the *nontermination behavior* of the command is independent of w . This is formalized by the following definition which states the condition under which an independence correctly describes a concrete trace.

Definition 3.1 *For all $\top \in \mathbf{Trc}$, for all $x \in \mathbf{Var} \cup \{\perp\}$, for all $w \in \mathbf{Var}$, $\top \models [x \times w]$ holds iff for all $s_1, s_2 \in \mathbf{Sto}$: $s_1 \stackrel{w}{=} s_2$ implies $\top s_1 \stackrel{x}{=} \top s_2$.*

$\top \models \top^\#$ holds iff for all $[x \times w] \in \top^\#$ it holds that $\top \models [x \times w]$.

In the definition of $\top \models [x \times w]$, note that s_1, s_2 are the “initial” stores and the antecedent of the implication asserts that these stores are equal except for w . The “current” stores are obtained via $\top s_1$ and $\top s_2$ and the consequent of the implication demands that the current stores be equal on x . Because x can be \perp , and because of the way $[\perp \times w]$ is defined, an analysis based on Definition 3.1 is *nontermination sensitive*. In particular, observe the following result:

Fact 3.2 *Assume that $\top \models \{[x \times w], [\perp \times w]\}$. Then for all $s_1, s_2 \in \mathbf{Sto}$, $s_1 \stackrel{w}{=} s_2$ implies that either $\top s_1 = \top s_2 = \perp$, or $\top s_1 \neq \perp$ and $\top s_2 \neq \perp$ with $\top s_1 x = \top s_2 x$.*

Definition 3.3 *The ordering $\top_1^\# \preceq \top_2^\#$ holds iff $\top_2^\# \subseteq \top_1^\#$.*

The intuition behind the definition is that in $\top_1^\# \preceq \top_2^\#$, $\top_1^\#$ is more precise than $\top_2^\#$ because $\top_1^\#$ deems more variables independent than $\top_2^\#$. A motivation for the definition is the desire for a subtyping rule, stating that if $\top_1^\# \preceq \top_2^\#$ then $\top_1^\#$ can be replaced by $\top_2^\#$ (c.f. Fact 3.4). The subtyping rule is sound provided $\top_2^\#$ is a subset of $\top_1^\#$ and therefore obtainable from $\top_1^\#$ by removing information.

Clearly, **Independ** forms a complete lattice wrt. the ordering \preceq ; let $\sqcap_i T_i^\#$ denote the greatest lower bound (which is the set union). The greatest element is the empty set of independences and the least element is **Independ**; the least element is obtained when all variables have constant values. We have some expected properties, where Fact 3.4 is used in the proof of the correctness theorem (Section 5), and where Fact 3.5 follows since if $[x \times w]$ belongs to $\sqcap_i T_i^\#$ then it also belongs to some $T_i^\#$.

Fact 3.4 *If $T \models T_1^\#$ and $T_1^\# \preceq T_2^\#$ then $T \models T_2^\#$.*

Fact 3.5 *If for all $i \in I$ it holds that $T \models T_i^\#$, then $T \models \sqcap_{i \in I} T_i^\#$.*

We have the following fact about the identity trace.

Fact 3.6 *For all $x \in \mathbf{Var} \cup \{\perp\}$, for all $y \in \mathbf{Var}$, if $x \neq y$ then $\lambda s.s \models [x \times y]$.*

Do we have an abstract interpretation? Facts 3.4 and 3.5 lead us to explore the connection of our framework with abstract interpretation [10]. We can write a function $\gamma : \mathbf{Independ} \rightarrow \mathcal{P}(\mathbf{Trc})$:

$$\gamma(T^\#) = \{T \in \mathbf{Trc} \mid T \models T^\#\}$$

and demonstrate that γ is completely multiplicative. We calculate:

$$\begin{aligned} T \in \gamma(\sqcap_i T_i^\#) &\Leftrightarrow \forall [x \times y] \in \sqcap_i T_i^\# \bullet T \models [x \times y] \\ &\Leftrightarrow \forall i \bullet \forall [x \times y] \in T_i^\# \bullet T \models [x \times y] \\ &\Leftrightarrow \forall i \bullet T \in \gamma(T_i^\#) \\ &\Leftrightarrow T \in \bigcap_i \gamma(T_i^\#) \end{aligned}$$

Therefore, γ can be considered a concretization function so that, by properties of Galois Connections, e.g., [21, Lemma 4.23], there exists an abstraction function $\alpha : \mathcal{P}(\mathbf{Trc}) \rightarrow \mathbf{Independ}$:

$$\alpha(U) = \bigcup \{T^\# \mid U \subseteq \gamma(T^\#)\}$$

such that $(\mathcal{P}(\mathbf{Trc}), \alpha, \gamma, \mathbf{Independ})$ is a Galois connection.

Equivalently, Facts 3.4 and 3.5 state that relation \models is \mathbf{U} -closed (upwards closed) and \mathbf{G} -closed (greatest lower bound closed) respectively. Therefore, \models defines a Galois connection [30]. It follows that by defining $\gamma(T^\#)$ as above, we have the properties:

- γ is a monotone function.
- For all $T^\# \in \mathbf{Independ}$, for all $U \in \mathcal{P}(\mathbf{Trc})$, $U \subseteq \gamma(T^\#)$ iff $\alpha(U) \preceq T^\#$.

The second item above is the definition of a Galois connection and in this sense, \models “is” a Galois connection.

4 Static Checking of Independences

To statically check independences we define, in Fig. 3, a Hoare-like logic where judgements are of the form

$$G \vdash \{T_0^\#\} C \{T^\#\}$$

The judgement is interpreted as saying that if the independences in $T_0^\#$ hold *before* execution of C then the independences in $T^\#$ will hold *after* execution of C . The context $G \in \mathbf{Context} = \mathcal{P}(\mathbf{Var})$ is a *control dependence*, denoting (a superset of) the variables that at least one test surrounding C depends on. For example, in **if** x **then** $y := 0$ **else** $z := 1$, the static checking of $y := 0$ takes place in the context that contains all variables that x is dependent on. This is crucial, especially since x may depend on a high variable.

We now explain a few of the rules in Fig. 3. Checking an assignment, $x := E$, in context G , involves checking any $[y \times w]$ in the postcondition $T^\#$. There are two cases. If $x \neq y$, then $[y \times w]$ must also appear in the precondition $T_0^\#$. Likewise, if $[\perp \times w]$ appears in the postcondition $T^\#$, then it must also appear in the precondition $T_0^\#$. Otherwise, if $x = y$ then $[x \times w]$ appears in the postcondition provided all variables referenced in E are independent of w ; moreover, w must not appear in G , as otherwise, x would be control dependent on w .

Checking a conditional, **if** E **then** C_1 **else** C_2 , involves checking C_1 and C_2 in a context G_0 that includes not only the “old” context G but also the variables that E depends on (as variables modified in C_1 or C_2 will be control dependent on the variables that E depends on.) Equivalently, if w is not in G_0 , then all free variables x in E must be independent of w , that is, $[x \times w]$ must appear in the precondition $T_0^\#$.

Checking a while loop is similar to checking a conditional. The only difference is that it requires guessing an “invariant” $T^\#$ that is both the precondition and the postcondition of the loop and its body. With respect to non-termination, note that if $w \in G_0$ then w may influence the termination of the program: either directly, as in **while** $w > 7$ **do** $w := w + 1$ (where the side condition for [While] forces w to be added to G_0); or indirectly, as in

$$\begin{array}{c}
\text{if } \forall [\mathbf{y} \times \mathbf{w}] \in \mathbf{T}^\# \bullet \\
[\text{Assign}] \quad \frac{G \vdash \{\mathbf{T}_0^\#\} \mathbf{x} := \mathbf{E} \{\mathbf{T}^\#\} \quad (x \neq y \vee y = \perp) \Rightarrow [\mathbf{y} \times \mathbf{w}] \in \mathbf{T}_0^\#}{G \vdash \{\mathbf{T}_0^\#\} \mathbf{x} := \mathbf{E} \{\mathbf{T}^\#\} \quad x = y \Rightarrow (\mathbf{w} \notin G \wedge \forall z \in \text{fv}(\mathbf{E}) \bullet [z \times \mathbf{w}] \in \mathbf{T}_0^\#)} \\
[\text{Seq}] \quad \frac{G \vdash \{\mathbf{T}_0^\#\} \mathbf{C}_1 \{\mathbf{T}_1^\#\} \quad G \vdash \{\mathbf{T}_1^\#\} \mathbf{C}_2 \{\mathbf{T}_2^\#\}}{G \vdash \{\mathbf{T}_0^\#\} \mathbf{C}_1 ; \mathbf{C}_2 \{\mathbf{T}_2^\#\}} \\
[\text{If}] \quad \frac{G_0 \vdash \{\mathbf{T}_0^\#\} \mathbf{C}_1 \{\mathbf{T}^\#\} \quad G_0 \vdash \{\mathbf{T}_0^\#\} \mathbf{C}_2 \{\mathbf{T}^\#\}}{G \vdash \{\mathbf{T}_0^\#\} \text{if } \mathbf{E} \text{ then } \mathbf{C}_1 \text{ else } \mathbf{C}_2 \{\mathbf{T}^\#\}} \text{ if } \quad G \subseteq G_0 \wedge \\
\quad (\mathbf{w} \notin G_0 \Rightarrow \forall x \in \text{fv}(\mathbf{E}) \bullet [x \times \mathbf{w}] \in \mathbf{T}_0^\#) \\
[\text{While}] \quad \frac{G_0 \vdash \{\mathbf{T}^\#\} \mathbf{C} \{\mathbf{T}^\#\}}{G \vdash \{\mathbf{T}^\#\} \text{while } \mathbf{E} \text{ do } \mathbf{C} \{\mathbf{T}^\#\}} \text{ if } \quad G \subseteq G_0 \wedge \\
\quad (\mathbf{w} \notin G_0 \Rightarrow \forall x \in \text{fv}(\mathbf{E}) \bullet [x \times \mathbf{w}] \in \mathbf{T}^\#) \wedge \\
\quad ([\perp \times \mathbf{w}] \in \mathbf{T}^\# \Rightarrow \mathbf{w} \notin G_0) \\
[\text{Sub}] \quad \frac{G_1 \vdash \{\mathbf{T}_1^\#\} \mathbf{C} \{\mathbf{T}_2^\#\}}{G_0 \vdash \{\mathbf{T}_0^\#\} \mathbf{C} \{\mathbf{T}_3^\#\}} \text{ if } (\mathbf{T}_0^\# \preceq \mathbf{T}_1^\#) \wedge (\mathbf{T}_2^\# \preceq \mathbf{T}_3^\#) \wedge (G_0 \subseteq G_1)
\end{array}$$

Fig. 3. The Logic.

if $w > 7$ then skip else while *true* do skip (where the side condition for [If] has already added w to G). Therefore we demand that if $[\perp \times w] \in \mathbf{T}^\#$ then w must not be in G_0 .

In Section 7, when we define *strongest postcondition*, we will select $G_0 = G \cup \{w \mid \exists x \in \text{fv}(\mathbf{E}) \bullet [x \times w] \notin \mathbf{T}_0^\#\}$ for the conditional and the while loop. Instead of guessing the invariant, we will show how to compute it using fix-points.

Example 4.1 (*Illustrating “recovery” of independences.*) We have the derivations

$$\begin{array}{l}
\emptyset \vdash \{[\mathbf{l} \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\} \mathbf{l} := \mathbf{h} \{[\mathbf{h} \times \mathbf{l}], [\mathbf{l} \times \mathbf{l}]\} \text{ and} \\
\emptyset \vdash \{[\mathbf{h} \times \mathbf{l}], [\mathbf{l} \times \mathbf{l}]\} \mathbf{l} := \mathbf{0} \{[\mathbf{h} \times \mathbf{l}], [\mathbf{l} \times \mathbf{l}], [\mathbf{l} \times \mathbf{h}]\}
\end{array}$$

and therefore also

$$\emptyset \vdash \{[\mathbf{l} \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\} \mathbf{l} := \mathbf{h} ; \mathbf{l} := \mathbf{0} \{[\mathbf{h} \times \mathbf{l}], [\mathbf{l} \times \mathbf{l}], [\mathbf{l} \times \mathbf{h}]\}.$$

With the intuition that \mathbf{l} stands for “low” or “public” and \mathbf{h} stands for “high” or “sensitive”, the derivation asserts that if \mathbf{l} is independent of \mathbf{h} before execution, then \mathbf{l} is independent of \mathbf{h} after execution. Thus $[\mathbf{l} \times \mathbf{h}]$ is an *invariant*

of the computation. By Definition 3.1, any trace of the program applied to initial stores that differ only in the value for \mathbf{h} , creates new stores that agree on the current value for \mathbf{l} . Thus the program is secure, although it contains an insecure sub-program: the independence $[\mathbf{l} \times \mathbf{h}]$ is lost after $\mathbf{l} := \mathbf{h}$, but recovered after $\mathbf{l} := 0$.

Example 4.2 (*Illustrating control dependence.*) The reader may check that the following informally annotated program gives rise to a derivation in our logic. Initially, \mathbf{G} is empty, and all variables are pairwise independent; we write $[\mathbf{x} \times \mathbf{y}, \mathbf{z}]$ to abbreviate $[\mathbf{x} \times \mathbf{y}], [\mathbf{x} \times \mathbf{z}]$.

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                                 $\{[\mathbf{l} \times \mathbf{h}, \mathbf{x}], [\mathbf{h} \times \mathbf{l}, \mathbf{x}], [\mathbf{x} \times \mathbf{l}, \mathbf{h}]\}$ 
 $\mathbf{x} := \mathbf{h}$                         $\{[\mathbf{l} \times \mathbf{h}, \mathbf{x}], [\mathbf{h} \times \mathbf{l}, \mathbf{x}], [\mathbf{x} \times \mathbf{l}, \mathbf{x}]\}$ 
 $\text{if } \mathbf{x} > 0$                     $(\mathbf{G} \text{ is now } \{\mathbf{h}\})$ 
     $\text{then } \mathbf{l} := 7$             $\{[\mathbf{l} \times \mathbf{x}, \mathbf{l}], [\mathbf{h} \times \mathbf{l}, \mathbf{x}], [\mathbf{x} \times \mathbf{l}, \mathbf{x}]\}$ 
     $\text{else } \mathbf{x} := 0$            $\{[\mathbf{l} \times \mathbf{h}, \mathbf{x}], [\mathbf{h} \times \mathbf{l}, \mathbf{x}], [\mathbf{x} \times \mathbf{l}, \mathbf{x}]\}$ 
 $\text{end of if}$                     $\{[\mathbf{l} \times \mathbf{x}], [\mathbf{h} \times \mathbf{l}, \mathbf{x}], [\mathbf{x} \times \mathbf{l}, \mathbf{x}]\}$ 

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A few remarks:

- in the preamble, only \mathbf{x} is assigned, so the independences for \mathbf{l} and \mathbf{h} are carried through, but $[\mathbf{x} \times \mathbf{l}, \mathbf{x}]$ holds afterwards, as $[\mathbf{h} \times \mathbf{l}, \mathbf{x}]$ holds beforehand;
- the free variable in the guard is independent of \mathbf{l} and \mathbf{x} but not of \mathbf{h} , implying that \mathbf{h} has to be in \mathbf{G} .

Example 4.3 (*Illustrating how the logic works with nontermination.*)

With $\mathbf{P} = \mathbf{while} \ \mathbf{l} \neq 0 \ \mathbf{do} \ \mathbf{h} := 7$ and $\mathbf{T}^\# = \{[\perp \times \mathbf{h}], [\mathbf{l} \times \mathbf{h}]\}$ we have the judgement $\emptyset \vdash \{\mathbf{T}^\#\} \mathbf{P} \{\mathbf{T}^\#\}$ (since $\{\mathbf{l}\} \vdash \{\mathbf{T}^\#\} \ \mathbf{h} := 7 \ \{\mathbf{T}^\#\}$) showing that \mathbf{P} is deemed secure by our logic; an observer able to observe even nontermination cannot detect the initial value of \mathbf{h} . (The reason why $[\mathbf{h} \times \mathbf{l}]$ and $[\perp \times \mathbf{l}]$ are not in $\mathbf{T}^\#$ is that \mathbf{h} clearly depends on \mathbf{l} and nontermination depends on \mathbf{l} .)

However, with $\mathbf{P} = \mathbf{while} \ \mathbf{h} \neq 0 \ \mathbf{do} \ \mathbf{h} := 7$ and $\mathbf{T}^\# = \{[\perp \times \mathbf{h}], [\mathbf{l} \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\}$ we do not have a derivation, even though $\mathbf{T}^\#$ is an invariant for $\mathbf{h} := 7$. But because the derivation requires $\mathbf{h} \in \mathbf{G}_0$, $[\perp \times \mathbf{h}]$ can no longer be in $\mathbf{T}^\#$. This suggests that \mathbf{P} is insecure when nontermination is observable: indeed, an observer able to observe nontermination can detect whether \mathbf{h} was initially 0 or not. Interestingly, if nontermination is *not* observable, then $\mathbf{T}^\# = \{[\mathbf{l} \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\}$ remains invariant, as expected. In particular, the current value of \mathbf{l} is independent of the initial value of \mathbf{h} . Similarly, we do not

have a derivation for $P = \mathbf{if\ h \neq 0\ then\ skip\ else\ while\ true\ do\ skip}$ with $T^\#$ containing $[\perp \times h]$.

A couple of simple results can be proven about the logic in Fig. 3:

Fact 4.4 *Assume $G \vdash \{T_0^\#\} C \{T^\#\}$. If $[\perp \times w] \in T^\#$ then $[\perp \times w] \in T_0^\#$.*

The proof is an easy induction on a derivation of $G \vdash \{T_0^\#\} C \{T^\#\}$.

Lemma 4.5 *Assume $G \vdash \{T_0^\#\} C \{T^\#\}$ with $[\perp \times w] \in T^\#$ and $w \in G$. Then $T\ s \neq \perp$ implies $\llbracket C \rrbracket T\ s \neq \perp$.*

PROOF. An easy induction in the derivation, where for [Seq] we use Fact 4.4; for [While] the result follows vacuously since having both $[\perp \times w] \in T^\#$ and $w \in G$ violates the side conditions of that rule.

To restate Fact 4.4, if $[\perp \times w] \notin T_0^\#$, then $[\perp \times w]$ cannot be recovered in $T^\#$. This is in contrast to Example 4.1, which shows that although $[l \times h]$ does not appear in the precondition of $l := 0$, it could nonetheless be recovered in the postcondition of $l := 0$. In other words, once it is established that nontermination may depend on w , it continues to depend on w . This is what we should expect, as otherwise, with w interpreted “high”, nontermination can be used to leak the high value.

In a judgement $G \vdash \{T_0^\#\} C \{T^\#\}$, suppose $w \in G$. This means that any assignment in C is control dependent on w . Suppose now that y is a variable and is independent of w in the postcondition $T^\#$. This implies that y cannot be assigned to in C — otherwise, it would be dependent on w . If y is not assigned to in C , then y must be independent of w in the precondition too. These intuitions are collected together in Lemma 4.6 below. Note that with y interpreted as “low” and w as “high”, the lemma essentially says that low variables may not be written to under a high guard. Thus the lemma is the counterpart of the “no write down” rule that underlies information flow control; the term “*-property” [7] is also used. The value of low variables remains the same after execution of C .

Lemma 4.6 (Write Confinement) *Assume that $G \vdash \{T_0^\#\} C \{T^\#\}$. Then the following holds:*

*If $[y \times w] \in T^\#$ and $y \in \mathbf{Var}$ and $w \in G$
then $y \notin \mathbf{modified}(C)$ and $[y \times w] \in T_0^\#$.*

PROOF. We perform induction in the derivation of $G \vdash \{T_0^\#\} C \{T^\#\}$, and do a case analysis on the last rule applied:

[Assign], with $C = x := E$. If $x = y$, then $w \notin G$, contradicting our assumptions. If $x \neq y$, then $y \notin \text{modified}(C)$ and $[y \times w] \in T_0^\#$.

[Seq], with $C = C_1 ; C_2$. Assume that $G \vdash \{T_0^\#\} C_1 \{T_1^\#\}$ and that $G \vdash \{T_1^\#\} C_2 \{T^\#\}$ and also assume that $w \in G$. By applying the induction hypothesis to the latter judgement, we see that $y \notin \text{modified}(C_2)$ and that $[y \times w] \in T_1^\#$. By then applying the induction hypothesis to the former judgement, we see that $y \notin \text{modified}(C_1)$ and that $[y \times w] \in T_0^\#$. Therefore $y \notin \text{modified}(C)$, as desired.

[If], with $C = \text{if } E \text{ then } C_1 \text{ else } C_2$. Assume that

$$G_0 \vdash \{T_0^\#\} C_1 \{T^\#\} \text{ and } G_0 \vdash \{T_0^\#\} C_2 \{T^\#\}$$

where $G \subseteq G_0$. Let $[y \times w] \in T^\#$ with $w \in G$, then $w \in G_0$ so by applying the induction hypothesis we get $y \notin \text{modified}(C_1)$ and $y \notin \text{modified}(C_2)$ and $[y \times w] \in T_0^\#$. This implies $y \notin \text{modified}(C)$, and thereby the desired result.

[While], with $C = \text{while } E \text{ do } C_0$. Our assumptions are that $G \vdash \{T^\#\} C \{T^\#\}$ because with $G \subseteq G_0$ we have $G_0 \vdash \{T^\#\} C_0 \{T^\#\}$. Let $[y \times w] \in T^\#$ with $w \in G$, then $w \in G_0$ so by applying the induction hypothesis we get $y \notin \text{modified}(C_0)$ (and vacuously $[y \times w] \in T^\#$) which is as desired.

[Sub]. Assume that $G \vdash \{T_0^\#\} C \{T^\#\}$ because with $G \subseteq G_0$ and $T_0^\# \preceq T_1^\#$ and $T_2^\# \preceq T^\#$ we have $G_0 \vdash \{T_1^\#\} C \{T_2^\#\}$. Also assume that $[y \times w] \in T^\#$ and that $w \in G$. Then $[y \times w] \in T_2^\#$ and $w \in G_0$, so inductively we obtain $y \notin \text{modified}(C)$ and $[y \times w] \in T_1^\#$. Then also $[y \times w] \in T_0^\#$, as desired, because $T_0^\# \preceq T_1^\#$.

5 Correctness

We are now in a position to prove the correctness of the logic with respect to the trace semantics.

Theorem 5.1 *Assume that*

$G \vdash \{T_0^\#\} C \{T^\#\}$ *where for all* $[x \times y] \in T_0^\#$ *it is the case that* $x \neq y$.
Then $\llbracket C \rrbracket(\lambda s.s) \models T^\#$.

That is, $T^\#$ correctly describes the concrete trace obtained by executing command C on $\lambda s.s$, the initial trace.

The correctness theorem can be seen as the noninterference theorem for information flow. Indeed, consider the trace, T , obtained from executing the command C with the initial trace, $\lambda s.s$. With l and h interpreted as “low” and “high” respectively, suppose $[l \times h]$ appears in $T^\#$. Then for any two stores s_1, s_2 that differ only on h , the stores $T s_1$ and $T s_2$ must agree on the value of l , meaning that if both $T s_1$ and $T s_2$ terminate then they must not give different values for l . On the other hand, it is possible for either $T s_1$ or $T s_2$ or both to be \perp , as in the example

if h then $h := 7$ else (while true do skip).

which has $[l \times h]$ as an invariant.

Moreover, the correctness theorem says that if $[\perp \times h]$ appears in $T^\#$, then $T s_1 = \perp$ iff $T s_2 = \perp$: thus noninterference is preserved under nontermination.

To prove Theorem 5.1, we need the following main lemma. Then the theorem follows by substituting T by $\lambda s.s$, and then using Fact 3.6.

Lemma 5.2 *If $G \vdash \{T_0^\#\} C \{T^\#\}$ and $T \models T_0^\#$, then $\llbracket C \rrbracket T \models T^\#$.*

PROOF. We perform induction on a derivation of $G \vdash \{T_0^\#\} C \{T^\#\}$, and do a case analysis on the last rule applied.

[Assign], with $C = x := E$. We are given $[z \times w] \in T^\#$, and we consider $s_1, s_2 \in \mathbf{Sto}$ such that $s_1 \stackrel{w}{=} s_2$. With $T' = \llbracket C \rrbracket T$, we must show that $T' s_1 \stackrel{z}{=} T' s_2$.

If $z = \perp$, then $[z \times w] \in T_0^\#$, so $T s_1 \stackrel{\perp}{=} T s_2$. Since for all s , $T' s = \perp$ iff $T s = \perp$, this shows that also $T' s_1 \stackrel{\perp}{=} T' s_2$, as desired.

So now consider $z \in \mathbf{Var}$. If either $T s_1 = \perp$ or $T s_2 = \perp$, also $T' s_1 = \perp$ or $T' s_2 = \perp$, and the claim is trivial. We thus assume that $T s_1 \neq \perp$ and $T s_2 \neq \perp$. We have two subcases:

- (1) $z \neq x$: Then $[z \times w] \in T_0^\#$, so $T s_1 \stackrel{z}{=} T s_2$, that is, $T s_1 z = T s_2 z$. Since for all s we have $T' s z = T s z$, this shows that also $T' s_1 z = T' s_2 z$, as desired.
- (2) $z = x$: Then for all $y \in \text{fv}(E)$, we have $[y \times w] \in T_0^\#$ and therefore $T s_1 y = T s_2 y$. By Property 2.1 then $\llbracket E \rrbracket (T s_1) = \llbracket E \rrbracket (T s_2)$, showing that $T' s_1 x = T' s_2 x$, as desired.

[Seq], with $C = C_1 ; C_2$. Assume that

$$G \vdash \{T_0^\#\} C_1 \{T_1^\#\} \text{ and that } G \vdash \{T_1^\#\} C_2 \{T_2^\#\}.$$

By applying the induction hypothesis to the first judgement, we get

$$\llbracket C_1 \rrbracket(T) \models T_1^\#.$$

We then apply the induction hypothesis to the second judgement and get the desired result:

$$\llbracket C_2 \rrbracket(\llbracket C_1 \rrbracket(T)) \models T^\#.$$

[If], with $C = \text{if } E \text{ then } C_1 \text{ else } C_2$. Assume that

$$G_0 \vdash \{T_0^\#\} C_1 \{T^\#\} \text{ and } G_0 \vdash \{T_0^\#\} C_2 \{T^\#\}$$

where $w \notin G_0$ implies that $\forall x \in \text{fv}(E) \bullet [x \times w] \in T_0^\#$. Inductively, we can assume that $\llbracket C_1 \rrbracket T \models T^\#$ and $\llbracket C_2 \rrbracket T \models T^\#$. We are given $[z \times w] \in T^\#$ (where z might be \perp), and we consider $s_1, s_2 \in \mathbf{Sto}$ such that $s_1 \stackrel{w}{=} s_2$. With $T' = \llbracket C \rrbracket T$, we must show that $T' s_1 \stackrel{z}{=} T' s_2$.

First assume that at least one of $T s_1$ or $T s_2$ is \perp . By Fact 2.2, at least as many of $T' s_1$ and $T' s_2$ will be \perp . So if $z \in \mathbf{Var}$, then trivially $T' s_1 \stackrel{z}{=} T' s_2$. If $z = \perp$, then by Fact 4.4 we know that $[\perp \times w] \in T_0^\#$, so from $T \models T_0^\#$ we get $T s_1 \stackrel{\perp}{=} T s_2$ and therefore $T s_1 = T s_2 = \perp$. But then also $T' s_1 = T' s_2 = \perp$, showing $T' s_1 \stackrel{\perp}{=} T' s_2$.

We can thus assume that there exists stores $s'_1, s'_2 \neq \perp$ such that $s'_1 = T s_1$ and $s'_2 = T s_2$. Apart from symmetry, there are two cases:

true?($\llbracket E \rrbracket s'_1$) and ***true?***($\llbracket E \rrbracket s'_2$). Then $T' s_1 = \llbracket C_1 \rrbracket T s_1$ and $T' s_2 = \llbracket C_1 \rrbracket T s_2$. From $\llbracket C_1 \rrbracket T \models T^\#$ and $[z \times w] \in T^\#$ we infer that $\llbracket C_1 \rrbracket T s_1 \stackrel{z}{=} \llbracket C_1 \rrbracket T s_2$, which amounts to the desired $T' s_1 \stackrel{z}{=} T' s_2$.

true?($\llbracket E \rrbracket s'_1$) but not ***true?***($\llbracket E \rrbracket s'_2$). We claim

$$w \in G_0$$

and prove the claim by contradiction: suppose $w \notin G_0$. Then by the logic, for all $x \in \text{fv}(E)$, $[x \times w] \in T_0^\#$ implying $T s_1 \stackrel{x}{=} T s_2$, that is $s'_1(x) = s'_2(x)$. By Property 2.1 this implies $\llbracket E \rrbracket s'_1 = \llbracket E \rrbracket s'_2$ – a contradiction.

Having established $w \in G_0$, consider two cases. First assume $z = \perp$. Then (by Lemma 4.5 applied to the sub-derivations) we infer that $\llbracket C_1 \rrbracket T s_1 \neq \perp$ and

$\llbracket C_2 \rrbracket T s_2 \neq \perp$, and thus $T' s_1 \neq \perp$ and $T' s_2 \neq \perp$. But this yields the desired relation $T' s_1 \stackrel{\perp}{=} T' s_2$.

Now assume that $z \in \mathbf{Var}$. By Lemma 4.6 applied to the sub-derivations, we infer that

$$[z \times w] \in T_0^\# \text{ and } z \notin \text{modified}(C_1) \text{ and } z \notin \text{modified}(C_2).$$

Since $T \models T_0^\#$, this shows $T s_1 \stackrel{z}{=} T s_2$, that is, $T s_1 z = T s_2 z$. We know that $T' s_1 = \llbracket C_1 \rrbracket T s_1$ and $T' s_2 = \llbracket C_2 \rrbracket T s_2$. If either is \perp , the claim vacuously holds; so assume neither is \perp . By Lemma 2.3, $T s_1 z = \llbracket C_1 \rrbracket T s_1 z$ and $T s_2 z = \llbracket C_2 \rrbracket T s_2 z$. This shows the desired $T' s_1 z = T s_1 z = T s_2 z = T' s_2 z$.

[While], with $C = \mathbf{while} E \mathbf{do} C_0$. Our assumptions are that

$$G \vdash \{T^\#\} C \{T^\#\}$$

because with G_0 such that $w \notin G_0$ implies that $\forall x \in \text{fv}(E) \bullet [x \times w] \in T^\#$, and such that $[\perp \times w] \in T^\#$ implies $w \notin G_0$, we have

$$G_0 \vdash \{T^\#\} C_0 \{T^\#\}.$$

We define an auxiliary predicate \mathcal{P} :

$$\mathcal{P}(f) \Leftrightarrow \forall T \bullet (T \models T^\# \Rightarrow f T \models T^\#)$$

We shall establish

$$\forall i \geq 0 \bullet \mathcal{P}(f_i) \tag{1}$$

and do so by induction in i . For the base case, note that $f_0(T) = \lambda s. \perp$, and that $\lambda s. \perp \models T^\#$ always holds because no matter whether $z = \perp$ or not, $\perp \stackrel{z}{=} \perp$.

For the inductive case, we assume that $T \models T^\#$ and must prove $\mathcal{F} f T \models T^\#$, with f an iterand. So let $[z \times w] \in T^\#$ and $s_1 \stackrel{z}{=} w s_2$; we know that $T s_1 \stackrel{z}{=} T s_2$ and must prove $\mathcal{F} f T s_1 \stackrel{z}{=} \mathcal{F} f T s_2$.

First assume that at least one of $T s_1$ or $T s_2$ is \perp . By Fact 2.2, at least as many of $\mathcal{F} f T s_1$ and $\mathcal{F} f T s_2$ will be \perp . So if $z \in \mathbf{Var}$, then trivially $\mathcal{F} f T s_1 \stackrel{z}{=} \mathcal{F} f T s_2$. If $z = \perp$, then from $[\perp \times w] \in T^\#$ and $T \models T^\#$ we get $T s_1 \stackrel{\perp}{=} T s_2$ and therefore $T s_1 = T s_2 = \perp$. But then also $\mathcal{F} f T s_1 = \mathcal{F} f T s_2 = \perp$, showing $\mathcal{F} f T s_1 \stackrel{\perp}{=} \mathcal{F} f T s_2$.

We can thus assume that there exists stores $s'_1, s'_2 \neq \perp$ such that $s'_1 = T s_1$ and $s'_2 = T s_2$. Apart from symmetry, there are three cases:

true?($\llbracket E \rrbracket s'_1$) and ***true?***($\llbracket E \rrbracket s'_2$). Then $\mathcal{F} f T s_1 = f(\llbracket C_0 \rrbracket T) s_1$ and $\mathcal{F} f T s_2 = f(\llbracket C_0 \rrbracket T) s_2$. By applying the overall induction hypothesis on $G_0 \vdash \{T^\#\} C_0 \{T^\#\}$, we get $\llbracket C_0 \rrbracket T \models T^\#$; by applying the innermost induction hypothesis, we then get $f\llbracket C_0 \rrbracket T \models T^\#$, so that $f(\llbracket C_0 \rrbracket T) s_1 \stackrel{z}{=} f(\llbracket C_0 \rrbracket T) s_2$ which amounts to the desired $\mathcal{F} f T s_1 \stackrel{z}{=} \mathcal{F} f T s_2$.

not true?($\llbracket E \rrbracket s'_1$) and ***not true?***($\llbracket E \rrbracket s'_2$). Then $\mathcal{F} f T s_1 = T s_1$ and $\mathcal{F} f T s_2 = T s_2$, and the claim is trivial.

true?($\llbracket E \rrbracket s'_1$) but ***not true?***($\llbracket E \rrbracket s'_2$). If $w \notin G_0$, then by the logic, for all $x \in \text{fv}(E)$, $[x \times w] \in T_0^\#$ implying $T s_1 \stackrel{x}{=} T s_2$, that is $s'_1(x) = s'_2(x)$. By Property 2.1 this implies $\llbracket E \rrbracket s'_1 = \llbracket E \rrbracket s'_2$ – a contradiction. Thus $w \in G_0$. Since $[z \times w] \in T^\#$, we infer from the logic that $z \neq \perp$. Thus, z is a variable.

By Lemma 4.6 (applied to the sub-derivation), we infer that $z \notin \text{modified}(C_0)$. We know that $\mathcal{F} f T s_1 = f(\llbracket C_0 \rrbracket T) s_1$ and $\mathcal{F} f T s_2 = T s_2$. We can assume that $\mathcal{F} f T s_1 \neq \perp$, as otherwise the claim vacuously holds. By Lemma 2.3, $\llbracket C_0 \rrbracket T s_1 z = T s_1 z$, and, by the same lemma, $f(\llbracket C_0 \rrbracket T) s_1 z = \llbracket C_0 \rrbracket T s_1 z$. So we have the desired relation:

$$\mathcal{F} f T s_1 z = f(\llbracket C_0 \rrbracket T) s_1 z = T s_1 z = T s_2 z = \mathcal{F} f T s_2 z.$$

We have proved (1). We must now prove $\mathcal{P}(\llbracket C \rrbracket)$, that is $\mathcal{P}(\sqcup_i f_i)$. So assume that $T \models T^\#$ and that $[z \times w] \in T^\#$ and that $s_1 \stackrel{w}{=} s_2$; we must prove that $\llbracket C \rrbracket T s_1 \stackrel{z}{=} \llbracket C \rrbracket T s_2$. Clearly, there exists j such that $\llbracket C \rrbracket T s_1 = f_j T s_1$ and $\llbracket C \rrbracket T s_2 = f_j T s_2$. The claim now follows from (1).

[Sub]. Assume that

$$G \vdash \{T_0^\#\} C \{T^\#\}$$

because with $G \subseteq G_0$ and $T_0^\# \preceq T_1^\#$ and $T_2^\# \preceq T^\#$ we have

$$G_0 \vdash \{T_1^\#\} C \{T_2^\#\}.$$

From our assumption $T \models T_0^\#$ we infer by Fact 3.4 that $T \models T_1^\#$, so inductively we can assume that $\llbracket C \rrbracket T \models T_2^\#$. One more application of Fact 3.4 then yields the desired result.

6 An application of the logic: Forward Slicing

In this section we shall see how to compute the “low” forward slice of a program P . That is, we focus on one particular “high” variable h , and eliminate all

$$\begin{array}{l}
\text{[Assign]} \quad G \vdash \{T_0^\#\} x := E \{T^\#\} \quad \Rightarrow \\
\quad \left\{ \begin{array}{l} x := E, \text{ if } [x \times h] \in T^\# \\ \mathbf{skip}, \text{ if } [x \times h] \notin T^\# \end{array} \right. \\
\\
\text{[Seq]} \quad \frac{\overbrace{G \vdash \{T_0^\#\} C_1 \{T_1^\#\}}^{D_1} \quad \overbrace{G \vdash \{T_1^\#\} C_2 \{T_2^\#\}}^{D_2}}{G \vdash \{T_0^\#\} C_1 ; C_2 \{T_2^\#\}} \quad \Rightarrow \mathcal{S}(D_1) ; \mathcal{S}(D_2) \\
\\
\text{[If]} \quad \frac{\overbrace{G_0 \vdash \{T_0^\#\} C_1 \{T^\#\}}^{D_1} \quad \overbrace{G_0 \vdash \{T_0^\#\} C_2 \{T^\#\}}^{D_2}}{G \vdash \{T_0^\#\} \mathbf{if} E \mathbf{then} C_1 \mathbf{else} C_2 \{T^\#\}} \quad \Rightarrow \\
\quad \left\{ \begin{array}{l} \mathbf{if} E \mathbf{then} \mathcal{S}(D_1) \mathbf{else} \mathcal{S}(D_2), \text{ if } h \notin G_0 \\ \mathbf{skip}, \quad \quad \quad \text{if } h \in G_0 \end{array} \right. \\
\\
\text{[While]} \quad \frac{\overbrace{G_0 \vdash \{T^\#\} C \{T^\#\}}^{D_0}}{G \vdash \{T^\#\} \mathbf{while} E \mathbf{do} C \{T^\#\}} \quad \Rightarrow \\
\quad \left\{ \begin{array}{l} \mathbf{while} E \mathbf{do} \mathcal{S}(D_0), \text{ if } h \notin G_0 \\ \mathbf{skip}, \quad \quad \quad \text{if } h \in G_0 \end{array} \right. \\
\\
\text{[Sub]} \quad \frac{\overbrace{G_1 \vdash \{T_1^\#\} C \{T_2^\#\}}^{D_0}}{G_0 \vdash \{T_0^\#\} C \{T_3^\#\}} \quad \Rightarrow \mathcal{S}(D_0)
\end{array}$$

Fig. 4. Forward Slicing: the slicing function \mathcal{S} .

commands in P that may depend on h , yielding a “slice” P' . With $l_1 \dots l_n$ the variables not depending on h in P (and thus considered “low”), our aim is that P' should be equivalent to P on $l_1 \dots l_n$. Then a user, wanting to compute the low variables but (for security reasons) not given clearance to view h , could be given P' to run, rather than P .

The slicing function \mathcal{S} is defined in Fig. 4, inductively on derivations in the logic (Fig. 3.) The idea is to replace by **skip** (i) assignments to variables that may depend on h ; (ii) conditionals and loops whose test may depend on h .

With $s_1, s_2 \in \mathbf{Sto}_\perp$, we write $s_1 \stackrel{T^\#}{=} s_2$ to denote that $s_1 \stackrel{z}{=} s_2$ holds for all $z \in \mathbf{Var} \cup \{\perp\}$ such that $[z \times \mathbf{h}] \in T^\#$. We can now formulate correctness of slicing:

Theorem 6.1 *Let D be a derivation for the judgement $G \vdash \{T_0^\#\} C \{T^\#\}$, and let $\mathcal{S}(D) = C'$. Then $\llbracket C \rrbracket T s \stackrel{T^\#}{=} \llbracket C' \rrbracket T s$.*

In particular, if $T^\# = \{[l \times \mathbf{h}], [\perp \times \mathbf{h}]\}$ we can infer that either $\llbracket C \rrbracket T s$ and $\llbracket C' \rrbracket T s$ are both \perp , or they are both $\neq \perp$ and agree on l .

On the other hand, if $T^\#$ contains only $[l \times \mathbf{h}]$ we can infer only partial correctness; it may happen that $\llbracket C \rrbracket T s = \perp$ but $\llbracket C' \rrbracket T s \neq \perp$. For an example of that, consider the program **while** $\mathbf{h} > 0$ **do skip**; it can be given a derivation

$$\frac{\{\mathbf{h}\} \vdash \{[l \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\} \text{ skip } \{[l \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\}}{\emptyset \vdash \{[l \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\} \text{ while } \mathbf{h} > 0 \text{ do skip } \{[l \times \mathbf{h}], [\mathbf{h} \times \mathbf{l}]\}}$$

which by \mathcal{S} is transformed into **skip**.

Theorem 6.1 follows immediately from the following main lemma, facilitating a proof by induction.

Lemma 6.2 *Let D be a derivation for the judgement $G \vdash \{T_0^\#\} C \{T^\#\}$, and let $\mathcal{S}(D) = C'$.*

If $T_1 s \stackrel{T_0^\#}{=} T_2 s$ then $\llbracket C \rrbracket T_1 s \stackrel{T^\#}{=} \llbracket C' \rrbracket T_2 s$.

PROOF. Induction in the derivation, where in all cases, we can assume that

$$T_1 s \neq \perp \text{ and } T_2 s \neq \perp.$$

For if that is not the case, then (by Fact 2.2) we have $\llbracket C \rrbracket T_1 s = \perp$ or $\llbracket C' \rrbracket T_2 s = \perp$. We would therefore have the desired relation $\llbracket C \rrbracket T_1 s \stackrel{z}{=} \llbracket C' \rrbracket T_2 s$ for all $z \in \mathbf{Var}$, but must still consider the case $[\perp \times \mathbf{h}] \in T^\#$. Then by Fact 4.4 we infer that $[\perp \times \mathbf{h}] \in T_0^\#$ and therefore $T_1 s \stackrel{\perp}{=} T_2 s$ implying $T_1 s = T_2 s = \perp$; by Fact 2.2 this implies $\llbracket C \rrbracket T_1 s = \llbracket C' \rrbracket T_2 s = \perp$ and therefore the desired relation $\llbracket C \rrbracket T_1 s \stackrel{\perp}{=} \llbracket C' \rrbracket T_2 s$.

We now embark on a case analysis.

[Assign]. Given $[z \times \mathbf{h}] \in T^\#$, we must show

$$\llbracket C \rrbracket T_1 s \stackrel{z}{=} \llbracket C' \rrbracket T_2 s.$$

If $z = \perp$, the claim is trivial, since $\llbracket C \rrbracket T_1 s \neq \perp$ and $\llbracket C' \rrbracket T_2 s \neq \perp$. So assume that $z \neq \perp$.

If $z \neq x$, then $[z \times h] \in T_0^\#$, implying $T_1 s z = T_2 s z$. So no matter whether C' is $x := E$ or **skip**, we have the desired equality

$$\llbracket C \rrbracket T_1 s z = T_1 s z = T_2 s z = \llbracket C' \rrbracket T_2 s z.$$

Now assume that $z = x$, in which case Fig. 4 tells us that $C' = C$, and where the side conditions from Fig. 3 tell us that for all $y \in \text{fv}(E)$, $[y \times h] \in T_0^\#$ and thus $T_1 s y = T_2 s y$. Therefore, using Property 2.1, $\llbracket E \rrbracket (T_1 s) = \llbracket E \rrbracket (T_2 s)$. Again we therefore get the desired equality

$$\llbracket C \rrbracket T_1 s z = \llbracket E \rrbracket (T_1 s) = \llbracket E \rrbracket (T_2 s) = \llbracket C' \rrbracket T_2 s z.$$

[Seq]. Assume that with C of the form $C_1 ; C_2$, the derivation D of $G \vdash \{T_0^\#\} C \{T^\#\}$ has two children: a derivation D_1 of a judgement $G \vdash \{T_0^\#\} C_1 \{T_1^\#\}$, and a derivation D_2 of a judgement $G \vdash \{T_1^\#\} C_2 \{T^\#\}$. With $C'_1 = \mathcal{S}(D_1)$ and $C'_2 = \mathcal{S}(D_2)$, we have $C' = \mathcal{S}(D) = C'_1 ; C'_2$.

Inductively on D_1 , we from $T_1 s \stackrel{T_0^\#}{=} T_2 s$ infer $\llbracket C_1 \rrbracket T_1 s \stackrel{T_1^\#}{=} \llbracket C'_1 \rrbracket T_2 s$; inductively on D_2 , we further infer $\llbracket C_2 \rrbracket (\llbracket C_1 \rrbracket T_1 s) \stackrel{T^\#}{=} \llbracket C_2 \rrbracket (\llbracket C'_1 \rrbracket T_2 s)$ which amounts to the desired equality $\llbracket C \rrbracket T_1 s \stackrel{T^\#}{=} \llbracket C' \rrbracket T_2 s$.

[Sub]. Assume that the derivation D of $G \vdash \{T_0^\#\} C \{T^\#\}$ has as child a derivation D_0 of a judgment $G_0 \vdash \{T_1^\#\} C \{T_2^\#\}$, where $G \subseteq G_0$ and $T_0^\# \preceq T_1^\#$ and $T_2^\# \preceq T^\#$. Here $C' = \mathcal{S}(D) = \mathcal{S}(D_0)$. Our assumption $T_1 s \stackrel{T_0^\#}{=} T_2 s$ clearly implies $T_1 s \stackrel{T_1^\#}{=} T_2 s$ (as $T_1^\# \subseteq T_0^\#$); inductively on D_0 we can thus infer that $\llbracket C \rrbracket T_1 s \stackrel{T_2^\#}{=} \llbracket C' \rrbracket T_2 s$ which (as $T^\# \subseteq T_2^\#$) implies the desired equality $\llbracket C \rrbracket T_1 s \stackrel{T^\#}{=} \llbracket C' \rrbracket T_2 s$.

[If]. Assume that with C of the form **if** E **then** C_1 **else** C_2 , the derivation D of $G \vdash \{T_0^\#\} C \{T^\#\}$ has two children: a derivation D_1 of a judgement $G_0 \vdash \{T_0^\#\} C_1 \{T^\#\}$, and a derivation D_2 of a judgement $G_0 \vdash \{T_0^\#\} C_2 \{T^\#\}$. Let $C'_1 = \mathcal{S}(D_1)$, and $C'_2 = \mathcal{S}(D_2)$. Looking at Fig. 4, we see that there are two subcases:

$C' = \text{if } E \text{ then } C'_1 \text{ else } C'_2$, with $h \notin G_0$. The side condition in Fig. 3 tells us that for all $x \in \text{fv}(E)$ it holds that $[x \times h] \in T_0^\#$ and thus $T_1 s x = T_2 s x$. Therefore, using Property 2.1, $\llbracket E \rrbracket (T_1 s) = \llbracket E \rrbracket (T_2 s)$; we can assume wlog. that this value satisfies *true*?. Thus $\llbracket C \rrbracket T_1 s = \llbracket C_1 \rrbracket T_1 s$ and $\llbracket C' \rrbracket T_2 s = \llbracket C'_1 \rrbracket T_2 s$;

this amounts to the desired equality $\llbracket C \rrbracket_{T_1} s \stackrel{T^\#}{=} \llbracket C' \rrbracket_{T_2} s$ since inductively on D_1 we infer that $\llbracket C_1 \rrbracket_{T_1} s \stackrel{T^\#}{=} \llbracket C'_1 \rrbracket_{T_2} s$.

$C' = \mathbf{skip}$, **with** $h \in G_0$. Let $[z \times h] \in T^\#$ be given; we must show that

$$\llbracket C \rrbracket_{T_1} s \stackrel{z}{=} \llbracket C' \rrbracket_{T_2} s.$$

If $z = \perp$, Lemma 4.5 (applied to D_1 and D_2) tells us that $\llbracket C_1 \rrbracket_{T_1} s \neq \perp$ and $\llbracket C_2 \rrbracket_{T_1} s \neq \perp$; thus $\llbracket C \rrbracket_{T_1} s \neq \perp$, yielding the claim (since trivially, $\llbracket C' \rrbracket_{T_2} s \neq \perp$).

We thus now assume that $z \in \mathbf{Var}$; from Lemma 4.6 (applied to D_1 and D_2) we then infer that

- $z \notin \mathit{modified}(C_1)$ and $z \notin \mathit{modified}(C_2)$, and therefore $z \notin \mathit{modified}(C)$ which by Lemma 2.3 implies that $\llbracket C \rrbracket$ preserves z ;
- $[z \times h] \in T_0^\#$, implying that $T_1 s z = T_2 s z$.

If $\llbracket C \rrbracket_{T_1} s = \perp$, the claim is trivial; otherwise we from the above infer the desired equality:

$$\llbracket C \rrbracket_{T_1} s z = T_1 s z = T_2 s z = \llbracket C' \rrbracket_{T_2} s z.$$

[While]. Assume that with C of the form **while** E **do** C_0 , and with $T_0^\# = T^\#$, the derivation D of $G \vdash \{T_0^\#\} C \{T^\#\}$ has one child: a derivation D_0 of a judgement $G_0 \vdash \{T^\#\} C_0 \{T^\#\}$. Let $C'_0 = \mathcal{S}(D_0)$. Looking at Fig. 4, we see that there are two subcases:

$C' = \mathbf{while}$ E **do** C'_0 , **with** $h \notin G_0$. Let \mathcal{F} be the functor of C , let \mathcal{F}' be the function of C' , let f_i ($i \geq 0$) be the iterands of C , and let f'_i ($i \geq 0$) be the iterands of C' . We shall prove by induction in i that

$$\text{forall } T_1, T_2, s: T_1 s \stackrel{T^\#}{=} T_2 s \text{ implies } f_i T_1 s \stackrel{T^\#}{=} f'_i T_2 s. \quad (1)$$

In all cases, we can assume that $T_1 s \neq \perp$ and $T_2 s \neq \perp$. For if that is not the case, then (by Fact 2.2) we have $f_i T_1 s = \perp$ or $f'_i T_2 s = \perp$, which would imply the desired judgement, except if $[\perp \times h] \in T^\#$ in which case we must show that $f_i T_1 s = f'_i T_2 s = \perp$ which (by Fact 2.2) can be done by showing $T_1 s = T_2 s = \perp$. But this follows from $T_1 s \stackrel{\perp}{=} T_2 s$.

For $i = 0$, the claim is obvious, since $\perp \stackrel{T^\#}{=} \perp$ always holds. Now consider the inductive step, where we must prove that $T_1 s \stackrel{T^\#}{=} T_2 s$ implies $\mathcal{F} f_i T_1 s \stackrel{T^\#}{=} \mathcal{F}' f'_i T_2 s$. Since $h \notin G_0$, the side condition in Fig. 3 tells us that for all $x \in \text{fv}(E)$ it holds that $[x \times h] \in T^\#$ and thus $T_1 s x = T_2 s x$. Therefore,

using Property 2.1, $\llbracket E \rrbracket(T_1 s) = \llbracket E \rrbracket(T_2 s)$.

If this value does not satisfy *true?*, then $\mathcal{F} f_i T_1 s = T_1 s$ and $\mathcal{F}' f'_i T_2 s = T_2 s$, trivially implying the claim. Otherwise, our task is to prove that

$$f_i (\llbracket C_0 \rrbracket T_1) s \stackrel{T^\#}{=} f'_i (\llbracket C'_0 \rrbracket T_2) s.$$

By applying the overall induction hypothesis on D_0 , we infer that $\llbracket C_0 \rrbracket T_1 s \stackrel{T^\#}{=} \llbracket C'_0 \rrbracket T_2 s$; the claim then follows by applying the innermost induction hypothesis.

We can now return to our main task, trying to prove that $\llbracket C \rrbracket T_1 s \stackrel{T^\#}{=} \llbracket C' \rrbracket T_2 s$ (where we are given that $T_1 s \stackrel{T_0^\#}{=} T_2 s$). But observe that there exists j such that $\llbracket C \rrbracket T_1 s = f_j T_1 s$ and $\llbracket C' \rrbracket T_2 s = f'_j T_2 s$; therefore (1) yields the claim.

$C' = \mathbf{skip}$, with $\mathbf{h} \in G_0$. Let $[z \times \mathbf{h}] \in T^\#$ be given; we must show that

$$\llbracket C \rrbracket T_1 s \stackrel{z}{=} \llbracket C' \rrbracket T_2 s.$$

The side condition in Fig. 3 tells us that $z \in \mathbf{Var}$; from Lemma 4.6 (applied to D_0) we then infer that $z \notin \mathit{modified}(C_0) = \mathit{modified}(C)$ which by Lemma 2.3 implies that $\llbracket C \rrbracket$ preserves z . If $\llbracket C \rrbracket T_1 s = \perp$, the claim is trivial; otherwise, the desired equality follows from the calculation

$$\llbracket C \rrbracket T_1 s z = T_1 s z = T_2 s z = \llbracket C' \rrbracket T_2 s z$$

where the second equality follows from $[z \times \mathbf{h}] \in T^\#$.

Example 6.3 (*Illustrating forward slicing.*) For the program $\mathbf{l} := \mathbf{h} ; \mathbf{l} := 0$, the forward slice is $\mathbf{skip} ; \mathbf{l} := 0$, because we lose the independence $[\mathbf{l} \times \mathbf{h}]$ after $\mathbf{l} := \mathbf{h}$. For the program $\mathbf{h} := \mathbf{l} ; \mathbf{l} := \mathbf{h}$, the forward slice is $\mathbf{h} := \mathbf{l} ; \mathbf{l} := \mathbf{h}$ itself, since we retain the independence $[\mathbf{l} \times \mathbf{h}]$ after each of the commands in the sequence, and since the first command establishes the independence $[\mathbf{h} \times \mathbf{h}]$.

7 Computing Independences

In Fig. 5 we define a function

$$sp : \mathbf{Context} \times \mathbf{Cmd} \times \mathbf{Independ} \rightarrow \mathbf{Independ}$$

with the intuition (formalized below) that given a control dependence G , a command C and a precondition $T^\#$, $sp(G, C, T^\#)$ computes a postcondition

$T_1^\#$ such that $G \vdash \{T^\#\} C \{T_1^\#\}$ holds, and $T_1^\#$ is the “largest” set (wrt. the subset ordering) that makes the judgement hold. Thus we compute the “strongest provable postcondition”, which might differ⁴ from the strongest *semantic* postcondition, that is, the largest set $T_1^\#$ such that for all T , if $T \models T^\#$ then $\llbracket C \rrbracket(T) \models T_1^\#$.

In a companion technical report [3], we show how to also compute “weakest precondition”; we conjecture that the development in Sect. 8 could alternatively be carried out using weakest precondition instead of strongest postcondition.

We now explain two of the cases in Fig. 5. In an assignment, $x := E$, the postcondition carries over all independences $[y \times w]$ in the precondition if $y \neq x$; these independences are unaffected by the assignment to x . Suppose that w does not occur in context G . Then x is not control dependent on w . Moreover, if all variables referenced in E are independent of w , then $[x \times w]$ will be in the postcondition of the assignment.

The case for **while** is best explained by means of an example.

Example 7.1 Consider the program

$$C = n := 0 ; \mathbf{while} \ y > n \ \mathbf{do} \ l := x ; x := y ; y := h ; n := n + 1 .$$

Let $T_0^\# \dots T_{12}^\#$ be given by the following table, where V denotes the set $\{h, l, n, x, y\}$. For reasons of space, we will represent a non-empty set as the concatenation of its elements in the table – thus $\{h, l, n, x\}$ is represented as $hlnx$. For example, the entry in the column for $T_6^\#$ and in the row for x shows that $[x \times h] \in T_6^\#$

⁴ For example, let $C = l := h - h$ and $T^\# = \{[l \times h]\}$. Then $[l \times h]$ is in the strongest semantic postcondition, since for all T and all s_1, s_2 we have $\llbracket C \rrbracket T \stackrel{l}{=} \llbracket C \rrbracket T \ s_2$ and therefore $\llbracket C \rrbracket T \models [l \times h]$, but not in the strongest provable postcondition.

and $[x \times l] \in T_6^\#$ and $[x \times n] \in T_6^\#$.

	$T_0^\#$	$T_1^\#$	$T_2^\#$	$T_3^\#$	$T_4^\#$	$T_5^\#$	$T_6^\#$	$T_7^\#$	$T_8^\#$	$T_9^\#$	$T_{10}^\#$	$T_{11}^\#$	$T_{12}^\#$
$h \times$	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy	lnxy
$n \times$	hlxy	V	V	V	V	hlrx	hlrx	hlrx	hlrx	lnx	lnx	lnx	lnx
$l \times$	hnxy	hnxy	hln	hln	hln	hln	hn	ln	ln	ln	n	ln	ln
$x \times$	hlxy	hlxy	hlxy	hlrx	hlrx	hlrx	hln	hln	lnx	lnx	ln	ln	lnx
$y \times$	hlrx	hlrx	hlrx	hlrx	lnx	lnx	lnx	lnx	lnx	lnx	lnx	lnx	lnx
$\perp \times$	V	V	V	V	V	V	hlrx	hlrx	hlrx	hlrx	lnx	lnx	lnx

Our goal is to compute $sp(\emptyset, C, T_0^\#)$ and doing so involves the fixed point computation sketched below.

	Iteration		
	first	second	third
while $y > n$ do	$T_1^\#$	$T_6^\#$	$T_{10}^\#$
G_0 :	$\{y\}$	$\{h, y\}$	$\{h, y\}$
$l := x$	$T_2^\#$	$T_7^\#$	$T_{11}^\#$
$x := y$	$T_3^\#$	$T_8^\#$	$T_{12}^\#$
$y := h$	$T_4^\#$	$T_8^\#$	$T_{12}^\#$
$n := n + 1$	$T_5^\#$	$T_9^\#$	$T_{12}^\#$
$_ \cap T_1^\# \setminus [\perp \times G_0]$	$T_6^\#$	$T_{10}^\#$	$T_{10}^\#$

For example, the entry $T_9^\#$ in the column marked “second” and in the row marked “ $n := n + 1$ ”, denotes that $sp(\{h, y\}, n := n + 1, T_8^\#) = T_9^\#$.

Note that at the end of the first iteration, the independence set is $T_6^\#$ and $[l \times h]$ is still present; it takes a second iteration – the independence set is then $T_{10}^\#$ – to filter $[l \times h]$ out and thus detect insecurity. The third iteration affirms that $T_{10}^\#$ is indeed a fixed point (of the functional $\mathcal{H}_{\text{while}}^{T_1^\#, \emptyset}$ defined in Fig. 5).

$$\begin{aligned}
sp(G, x := E, T_0^\#) &= \\
&\quad \{[y \times w] \mid (y \neq x \vee y = \perp) \wedge [y \times w] \in T_0^\#\} \\
&\quad \cup \{[x \times w] \mid w \notin G \wedge \forall y \in \text{fv}(E) \bullet [y \times w] \in T_0^\#\} \\
sp(G, C_1 ; C_2, T_0^\#) &= sp(G, C_2, sp(G, C_1, T_0^\#)) \\
sp(G, \text{if } E \text{ then } C_1 \text{ else } C_2, T_0^\#) &= \\
\text{let } G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T_0^\#\} & \\
T_1^\# = sp(G_0, C_1, T_0^\#) & \\
T_2^\# = sp(G_0, C_2, T_0^\#) & \\
\text{in } T_1^\# \cap T_2^\# & \\
sp(G, \text{while } E \text{ do } C_0, T_0^\#) &= \\
\text{let } \mathcal{H}_C^{\top_0^\#, G} : \mathbf{Independ} \rightarrow \mathbf{Independ} \text{ be given by } (C = \text{while } E \text{ do } C_0) & \\
\mathcal{H}_C^{\top_0^\#, G}(T^\#) = & \\
\text{let } G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T^\#\} & \\
\text{in } (sp(G_0, C_0, T^\#) \cap T^\#) \setminus \{[\perp \times w] \mid w \in G_0\} & \\
\text{in } \text{lf}p(\mathcal{H}_C^{\top_0^\#, G}) &
\end{aligned}$$

Fig. 5. Strongest Postcondition.

Theorem 7.3 states the correctness of the function sp , that it indeed computes a postcondition. Then, Theorem 7.4 states that the postcondition computed by sp is the strongest postcondition. We shall rely on the following property:

Lemma 7.2 (Monotonicity) *For all C , the following holds (for all $G, G_1, T^\#, T_1^\#$):*

- (1) $sp(G, C, T^\#)$ is well-defined;
- (2) $\mathcal{H}_C^{\top_0^\#, G}$ (when C is a **while** loop) is a monotone function;
- (3) if $G \subseteq G_1$ then $sp(G, C, T^\#) \preceq sp(G_1, C, T^\#)$;
- (4) if $T^\# \preceq T_1^\#$ then $sp(G, C, T^\#) \preceq sp(G, C, T_1^\#)$.

PROOF. Induction in C , where the four parts of the lemma are proved simultaneously. We do a case analysis on C ; the only non-trivial case is where C is of the form **while** E **do** C_0 .

Using the induction hypothesis on C_0 , we infer that for all $T_0^\#, G$ it holds that $\mathcal{H}_C^{\top_0^\#, G}$ is a monotone function on the complete lattice **Independ**. Hence $\text{lf}p(\mathcal{H}_C^{\top_0^\#, G})$, and thus $sp(G, C, T_0^\#)$, is indeed well-defined.

Next assume that $T^\# \preceq T_1^\#$ and that $G \subseteq G_1$. Then clearly $\mathcal{H}_C^{\top_0^\#, G} \preceq \mathcal{H}_C^{\top_0^\#, G_1}$ (by the pointwise ordering) and therefore $\text{lf}p(\mathcal{H}_C^{\top_0^\#, G}) \preceq \text{lf}p(\mathcal{H}_C^{\top_0^\#, G_1})$ which amounts to the desired relation $sp(G, C, T^\#) \preceq sp(G_1, C, T_1^\#)$.

Theorem 7.3 For all $C, G, T_0^\#,$ it holds that $G \vdash \{T_0^\#\} C \{sp(G, C, T_0^\#)\}.$

PROOF. Go by structural induction on C ; we perform a case analysis.

$C = x := E.$ Let $T^\# = sp(G, C, T_0^\#),$ and assume $[z \times w] \in T^\#.$ There are two cases:

- if $z \neq x$ then $[z \times w] \in T_0^\#.$
- if $z = x$ then $w \notin G,$ and $\forall y \in \text{fv}(E) \bullet [y \times w] \in T_0^\#.$

This establishes $G \vdash \{T_0^\#\} C \{T^\#\}.$

$C = C_1 ; C_2.$ Assume that $sp(G, C_1 ; C_2, T_0^\#) = T^\#$ because $T^\# = sp(G, C_2, T_1^\#)$ where $T_1^\# = sp(G, C_1, T_0^\#).$ By the induction hypothesis on C_1 and on $C_2,$ we have

$$G \vdash \{T_0^\#\} C_1 \{T_1^\#\} \text{ and } G \vdash \{T_1^\#\} C_2 \{T^\#\}$$

from which we infer the desired relation $G \vdash \{T_0^\#\} C_1 ; C_2 \{T^\#\}.$

$C = \text{if } E \text{ then } C_1 \text{ else } C_2.$ Assume that $sp(G, \text{if } E \text{ then } C_1 \text{ else } C_2, T_0^\#) = T^\#$ because $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T_0^\#\}, T_1^\# = sp(G_0, C_1, T_0^\#)$ and $T_2^\# = sp(G_0, C_2, T_0^\#)$ and $T^\# = T_1^\# \cap T_2^\#.$ Inductively, we have

$$G_0 \vdash \{T_0^\#\} C_1 \{T_1^\#\} \text{ and } G_0 \vdash \{T_0^\#\} C_2 \{T_2^\#\}.$$

As $T^\# \subseteq T_1^\#$ and $T^\# \subseteq T_2^\#$ we have $T_1^\# \preceq T^\#$ and $T_2^\# \preceq T^\#;$ by [Sub], this implies

$$G_0 \vdash \{T_0^\#\} C_1 \{T^\#\} \text{ and } G_0 \vdash \{T_0^\#\} C_2 \{T^\#\}.$$

This establishes the desired $G \vdash \{T_0^\#\} \text{if } E \text{ then } C_1 \text{ else } C_2 \{T^\#\},$ since $G \subseteq G_0$ and $w \notin G_0$ implies $\forall x \in \text{fv}(E) \bullet [x \times w] \in T_0^\#.$

$C = \text{while } E \text{ do } C_0.$ Assume that $sp(G, C, T_0^\#) = T^\#$ so we want to prove $G \vdash \{T_0^\#\} C \{T^\#\}.$ We have $T^\# = \text{lfp}(\mathcal{H}_C^{T_0^\#, G}).$ By definition of a fixed point, $T^\# = \mathcal{H}_C^{T_0^\#, G}(T^\#).$ Thus

$$T^\# = (sp(G_0, C_0, T^\#) \cap T_0^\#) \setminus \{[\perp \times w] \mid w \in G_0\}$$

where $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T^\#\}.$ Hence $sp(G_0, C_0, T^\#) \preceq T^\#$ and $T_0^\# \preceq T^\#.$ We claim $G \vdash \{T^\#\} C \{T^\#\},$ which by [Sub] implies the desired $G \vdash \{T_0^\#\} C \{T^\#\}.$

It remains to prove the claim, $G \vdash \{T^\#\} C \{T^\#\}$. By the induction hypothesis on C_0 , $G_0 \vdash \{T^\#\} C_0 \{sp(G_0, C_0, T^\#)\}$ and by [Sub] therefore

$$G_0 \vdash \{T^\#\} C_0 \{T^\#\}.$$

Now we get $G \vdash \{T^\#\} C \{T^\#\}$ by an application of [While] because $G \subseteq G_0$, because $w \notin G_0$ implies $\forall x \in \text{fv}(E) \bullet [x \times w] \in T^\#$, and because if $w \in G_0$ then $[\perp \times w] \notin T^\#$.

Theorem 7.4 *For all judgements $G \vdash \{T_0^\#\} C \{T^\#\}$, $sp(G, C, T_0^\#) \preceq T^\#$.*

PROOF. We perform induction in the derivation of $G \vdash \{T_0^\#\} C \{T^\#\}$, and do a case analysis on the last rule applied:

[Sub]. Assume that $G \vdash \{T_0^\#\} C \{T^\#\}$ because with $G \subseteq G_1$ and $T_0^\# \preceq T_2^\#$ and $T_3^\# \preceq T^\#$ we have $G_1 \vdash \{T_2^\#\} C \{T_3^\#\}$. Applying the induction hypothesis on that derivation, we get

$$sp(G_1, C, T_2^\#) \preceq T_3^\#$$

and by Lemma 7.2 we get $sp(G, C, T_0^\#) \preceq sp(G_1, C, T_2^\#)$. This yields the desired relation

$$sp(G, C, T_0^\#) \preceq T_3^\# \preceq T^\#.$$

[Assign], with $C = x := E$. Assume that $G \vdash \{T_0^\#\} C \{T^\#\}$, and let $T_1^\# = sp(G, C, T_0^\#)$. We want $T_1^\# \preceq T^\#$. Accordingly, assume $[y \times w] \in T^\#$ to show $[y \times w] \in T_1^\#$. We have two cases:

- $x \neq y$. Then $[y \times w] \in T_0^\#$; hence $[y \times w] \in T_1^\#$ by the definition of sp .
- $x = y$. Then $w \notin G$ and $\forall z \in \text{fv}(E) \bullet [z \times w] \in T_0^\#$; hence $[y \times w] \in T_1^\#$ by the definition of sp .

[Seq], with $C = C_1 ; C_2$. Assume $G \vdash \{T_0^\#\} C \{T^\#\}$ because $G \vdash \{T_0^\#\} C_1 \{T_2^\#\}$ and $G \vdash \{T_2^\#\} C_2 \{T^\#\}$. By the induction hypothesis on these derivations,

$$sp(G, C_1, T_0^\#) \preceq T_2^\# \text{ and } sp(G, C_2, T_2^\#) \preceq T^\#$$

which by Lemma 7.2 enables us to infer that

$$sp(G, C_2, sp(G, C_1, T_0^\#)) \preceq T^\#.$$

This is as desired, since $sp(G, C_1 ; C_2, T_0^\#) = sp(G, C_2, sp(G, C_1, T_0^\#))$.

[If], with $C = \mathbf{if\ E\ then\ } C_1 \mathbf{\ else\ } C_2$. Assume that $G \vdash \{T_0^\#\} C \{T^\#\}$ because $G_1 \vdash \{T_0^\#\} C_1 \{T^\#\}$ and $G_1 \vdash \{T_0^\#\} C_2 \{T^\#\}$ where $G \subseteq G_1$ and where $w \notin G_1$ implies that $\forall x \in \text{fv}(E) \bullet [x \times w] \in T_0^\#$. Inductively, via the judgements for C_1 and C_2 , we obtain

$$sp(G_1, C_1, T_0^\#) \preceq T^\# \text{ and } sp(G_1, C_2, T_0^\#) \preceq T^\#.$$

Let $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T_0^\#\}$. Note that $G_0 \subseteq G_1$. Thus by Lemma 7.2 we get

$$sp(G_0, C_1, T_0^\#) \preceq T^\# \text{ and } sp(G_0, C_2, T_0^\#) \preceq T^\#.$$

This yields the claim since $sp(G, C, T_0^\#) = sp(G_0, C_1, T_0^\#) \cap sp(G_0, C_2, T_0^\#)$.

[While], with $C = \mathbf{while\ E\ do\ } C_0$. Assume that $G \vdash \{T^\#\} C \{T^\#\}$ because $G_1 \vdash \{T^\#\} C_0 \{T^\#\}$ where $G \subseteq G_1$, and where $w \notin G_1$ implies that $\forall x \in \text{fv}(E) \bullet [x \times w] \in T^\#$, and where $[\perp \times w] \in T^\#$ implies $w \notin G_1$. Assume $T_1^\# = sp(G, C, T^\#)$ to show $T_1^\# \preceq T^\#$. By the definition of sp , $T_1^\# = \text{lfp}(\mathcal{H}_C^{T^\#, G})$. Let $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T^\#\}$; then

$$\begin{aligned} \mathcal{H}_C^{T^\#, G}(T^\#) &= (sp(G_0, C_0, T^\#) \cap T^\#) \setminus \{[\perp \times w] \mid w \in G_0\} \\ &\supseteq (sp(G_1, C_0, T^\#) \cap T^\#) \setminus \{[\perp \times w] \mid w \in G_1\} = T^\# \end{aligned}$$

Here the set inclusion follows from the observation that $G_0 \subseteq G_1$ and an application of Lemma 7.2; the last equality follows since $w \in G_1$ implies $[\perp \times w] \notin T^\#$, and since the induction hypothesis tells us that $sp(G_1, C_0, T^\#) \preceq T^\#$.

This shows that $\mathcal{H}_C^{T^\#, G}$ is reductive at $T^\#$, that is $\mathcal{H}_C^{T^\#, G}(T^\#) \preceq T^\#$, so by Tarski's theorem we infer the desired relation $T_1^\# = \text{lfp}(\mathcal{H}_C^{T^\#, G}) \preceq T^\#$.

The following result, where we clearly cannot allow $y = \perp$, is useful for the developments in Sections 8:

Lemma 7.5 *Given C , and $y \in \mathbf{Var}$ with $y \notin \text{modified}(C)$. Then for all $T_0^\#, G, w$:*

$$[y \times w] \in T_0^\# \text{ implies } [y \times w] \in sp(G, C, T_0^\#).$$

PROOF. Go by structural induction on C ; we perform a case analysis. In each case, our assumption is that $y \notin \text{modified}(C)$ and that $[y \times w] \in T_0^\#$; we must show $[y \times w] \in sp(G, C, T_0^\#)$.

$C = x := E$. Our assumptions imply that $y \neq x$, from which the result trivially follows.

$C = C_1 ; C_2$. Since $\mathbf{y} \notin \text{modified}(C_1)$ we can apply the induction hypothesis on C_1 , giving us $[\mathbf{y} \times \mathbf{w}] \in \text{sp}(G, C_1, T_0^\#)$. Since $\mathbf{y} \notin \text{modified}(C_2)$ we can then apply the induction hypothesis on C_2 , giving us $[\mathbf{y} \times \mathbf{w}] \in \text{sp}(G, C_2, \text{sp}(G, C_1, T_0^\#))$. This yields the claim.

$C = \text{if } E \text{ then } C_1 \text{ else } C_2$. Since $\mathbf{y} \notin \text{modified}(C_1)$ and $\mathbf{y} \notin \text{modified}(C_2)$, we can apply the induction hypothesis twice, yielding (using the terminology in Fig. 5)

$$[\mathbf{y} \times \mathbf{w}] \in T_1^\# \text{ and } [\mathbf{y} \times \mathbf{w}] \in T_2^\#.$$

Since $\text{sp}(G, C, T_0^\#) = T_1^\# \cap T_2^\#$, this yields the claim.

$C = \text{while } E \text{ do } C_0$. Let $T^\# = \text{sp}(G, C, T_0^\#)$, then $T^\# = \text{lfp}(\mathcal{H}_C^{T_0^\#, G})$. Define

$$T_1^\# = T^\# \cup \{[\mathbf{y} \times \mathbf{w}]\}$$

and let G_0 be as in Fig. 5. By applying the induction hypothesis on C_0 (possible since $\mathbf{y} \notin \text{modified}(C_0)$) we get $[\mathbf{y} \times \mathbf{w}] \in \text{sp}(G_0, C_0, T_1^\#)$; since $[\mathbf{y} \times \mathbf{w}] \in T_0^\#$ and $\mathbf{y} \neq \perp$ this implies

$$[\mathbf{y} \times \mathbf{w}] \in \mathcal{H}_C^{T_0^\#, G}(T_1^\#). \quad (1)$$

Since $\mathcal{H}_C^{T_0^\#, G}$ is a monotone function (by Lemma 7.2), we from $T_1^\# \preceq T^\#$ infer that $\mathcal{H}_C^{T_0^\#, G}(T_1^\#) \preceq \mathcal{H}_C^{T_0^\#, G}(T^\#) = T^\#$ and thus

$$T^\# \subseteq \mathcal{H}_C^{T_0^\#, G}(T_1^\#). \quad (2)$$

Combining (1) and (2), we infer $T_1^\# \subseteq \mathcal{H}_C^{T_0^\#, G}(T_1^\#)$, that is $\mathcal{H}_C^{T_0^\#, G}(T_1^\#) \preceq T_1^\#$. This shows that $\mathcal{H}_C^{T_0^\#, G}$ is reductive at $T_1^\#$, so by Tarski's theorem we infer $T^\# = \text{lfp}(\mathcal{H}_C^{T_0^\#, G}) \preceq T_1^\#$, that is $T_1^\# \subseteq T^\#$. This demonstrates that $[\mathbf{y} \times \mathbf{w}] \in T^\#$, as desired.

8 Modularity and the Frame Rule

Define $\text{lhs}(T^\#) = \{\mathbf{y} \mid [\mathbf{y} \times \mathbf{w}] \in T^\#\}$. Then we have (proof to follow shortly)

Theorem 8.1 (Frame rule (I)) *Given $T_0^\#$ and C . Then for all $T^\#, G$:*

- (1) *If $\text{lhs}(T_0^\#) \subseteq \mathbf{Var}$ and $\text{lhs}(T_0^\#) \cap \text{modified}(C) = \emptyset$ then $\text{sp}(G, C, T^\# \cup T_0^\#) \supseteq \text{sp}(G, C, T^\#) \cup T_0^\#$.*

(2) If $\text{lhs}(\mathsf{T}_0^\#) \subseteq \mathbf{Var}$ and $\text{lhs}(\mathsf{T}_0^\#) \cap \text{fv}(\mathsf{C}) = \emptyset$
then $\text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\# \cup \mathsf{T}_0^\#) = \text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\#) \cup \mathsf{T}_0^\#$.

Note that the weaker premise in 1 does not imply the stronger consequence in 2, since (with $[z \times w]$ playing the role of $\mathsf{T}_0^\#$)

$$\begin{aligned} \text{sp}(\emptyset, x := y + z, \{[y \times w]\} \cup \{[z \times w]\}) &= \{[y \times w], [z \times w], [x \times w]\} \\ \text{sp}(\emptyset, x := y + z, \{[y \times w]\}) \cup \{[z \times w]\} &= \{[y \times w], [z \times w]\}. \end{aligned}$$

In separation logic [18,26], the frame rule is motivated by the desire for local reasoning: if C_1 and C_2 modify disjoint regions of a heap, reasoning about C_1 can be performed independently of the reasoning about C_2 . In our setting, a consequence of the frame rule is that when analyzing a command C occurring in a larger context, the relevant independences are the ones whose left hand sides occur in C .

Theorem 8.1 is proved by observing that part (1) follows from Lemmas 7.5 and 7.2; then part (2) follows using the following result:

Lemma 8.2 *Let $\mathsf{T}_0^\#$ and C be given, with $\text{lhs}(\mathsf{T}_0^\#) \subseteq \mathbf{Var}$ and $\text{lhs}(\mathsf{T}_0^\#) \cap \text{fv}(\mathsf{C}) = \emptyset$. Then for all $\mathsf{T}^\#$ and G , $\text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\# \cup \mathsf{T}_0^\#) \subseteq \text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\#) \cup \mathsf{T}_0^\#$.*

PROOF. Go by structural induction on C ; we perform a case analysis.

$\mathsf{C} = x := E$. The claim follows from the following calculation, using that $\text{lhs}(\mathsf{T}_0^\#) \cap \text{fv}(E) = \emptyset$ and that $x \notin \text{lhs}(\mathsf{T}_0^\#)$.

$$\begin{aligned} &\text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\# \cup \mathsf{T}_0^\#) \\ &= \{[y \times w] \mid y \neq x \wedge [y \times w] \in \mathsf{T}^\# \cup \mathsf{T}_0^\#\} \cup \\ &\quad \{[x \times w] \mid w \notin \mathsf{G} \wedge \forall y \in \text{fv}(E) \bullet [y \times w] \in \mathsf{T}^\# \cup \mathsf{T}_0^\#\} \\ &= \{[y \times w] \mid y \neq x \wedge [y \times w] \in \mathsf{T}^\# \cup \mathsf{T}_0^\#\} \cup \\ &\quad \{[x \times w] \mid w \notin \mathsf{G} \wedge \forall y \in \text{fv}(E) \bullet [y \times w] \in \mathsf{T}^\#\} \\ &= \mathsf{T}_0^\# \cup \{[y \times w] \mid y \neq x \wedge [y \times w] \in \mathsf{T}^\#\} \cup \\ &\quad \{[x \times w] \mid w \notin \mathsf{G} \wedge \forall y \in \text{fv}(E) \bullet [y \times w] \in \mathsf{T}^\#\} \\ &= \mathsf{T}_0^\# \cup \text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\#) \end{aligned}$$

$\mathsf{C} = \mathsf{C}_1 ; \mathsf{C}_2$. Using our induction hypothesis and Lemma 7.2, we get

$$\text{sp}(\mathsf{G}, \mathsf{C}, \mathsf{T}^\# \cup \mathsf{T}_0^\#) = \text{sp}(\mathsf{G}, \mathsf{C}_2, \text{sp}(\mathsf{G}, \mathsf{C}_1, \mathsf{T}^\# \cup \mathsf{T}_0^\#))$$

$$\begin{aligned} &\subseteq sp(G, C_2, sp(G, C_1, T^\#) \cup T_0^\#) \subseteq sp(G, C_2, sp(G, C_1, T^\#)) \cup T_0^\# \\ &= sp(G, C, T^\#) \cup T_0^\# \end{aligned}$$

C = if E then C₁ else C₂. Let

$$G_0 = G \cup \{\mathbf{w} \mid \exists x \in \text{fv}(E) \bullet [x \times \mathbf{w}] \notin T^\# \cup T_0^\#\}$$

where our assumptions imply that $\text{lhs}(T_0^\#) \cap \text{fv}(E) = \emptyset$ and therefore also

$$G_0 = G \cup \{\mathbf{w} \mid \exists x \in \text{fv}(E) \bullet [x \times \mathbf{w}] \notin T^\#\}.$$

Using the induction hypothesis, we now get

$$\begin{aligned} sp(G, C, T^\# \cup T_0^\#) &= sp(G_0, C_1, T^\# \cup T_0^\#) \cap sp(G_0, C_2, T^\# \cup T_0^\#) \\ &\subseteq (sp(G_0, C_1, T^\#) \cup T_0^\#) \cap (sp(G_0, C_2, T^\#) \cup T_0^\#) \\ &= (sp(G_0, C_1, T^\#) \cap sp(G_0, C_2, T^\#)) \cup T_0^\# \\ &= sp(G, C, T^\#) \cup T_0^\# \end{aligned}$$

C = while E do C₀. Let $H = \mathcal{H}_C^{T^\#, G}$ and let $H_0 = \mathcal{H}_C^{T^\# \cup T_0^\#, G}$, we must show that $\text{lfp}(H_0) \subseteq \text{lfp}(H) \cup T_0^\#$. Define $T_1^\# = \text{lfp}(H_0)$, and let

$$G_0 = G \cup \{\mathbf{w} \mid \exists x \in \text{fv}(E) \bullet [x \times \mathbf{w}] \notin T_1^\# \setminus T_0^\#\}$$

where our assumptions imply that $\text{lhs}(T_0^\#) \cap \text{fv}(E) = \emptyset$ and therefore also

$$G_0 = G \cup \{\mathbf{w} \mid \exists x \in \text{fv}(E) \bullet [x \times \mathbf{w}] \notin T_1^\#\}.$$

Using Lemma 7.2 and our induction hypothesis on C₀ we get (where $T_\perp^\#$ denotes $\{[\perp \times \mathbf{w}] \mid \mathbf{w} \in G_0\}$)

$$\begin{aligned} T_1^\# \setminus T_0^\# &= H_0(T_1^\#) \setminus T_0^\# \\ &= ((sp(G_0, C_0, T_1^\#) \cap (T^\# \cup T_0^\#)) \setminus T_\perp^\#) \setminus T_0^\# \\ &= (sp(G_0, C_0, T_1^\#) \cap T^\#) \setminus T_0^\# \setminus T_\perp^\# \\ &\subseteq (sp(G_0, C_0, (T_1^\# \setminus T_0^\#) \cup T_0^\#) \cap T^\#) \setminus T_0^\# \setminus T_\perp^\# \\ &\subseteq ((sp(G_0, C_0, (T_1^\# \setminus T_0^\#)) \cup T_0^\#) \cap T^\#) \setminus T_0^\# \setminus T_\perp^\# \\ &\subseteq (sp(G_0, C_0, (T_1^\# \setminus T_0^\#)) \cap T^\#) \setminus T_\perp^\# \\ &= H(T_1^\# \setminus T_0^\#) \end{aligned}$$

We have proved $H(T_1^\# \setminus T_0^\#) \subseteq T_1^\# \setminus T_0^\#$. This shows that H is reductive at $T_1^\# \setminus T_0^\#$, so by Tarski's theorem we infer $\text{lfp}(H) \subseteq T_1^\# \setminus T_0^\#$. This implies the

desired relation

$$lfp(H_0) = T_1^\# \subseteq (T_1^\# \setminus T_0^\#) \cup T_0^\# \subseteq lfp(H) \cup T_0^\#.$$

As a consequence of Theorem 8.1, we get the following result:

Corollary 8.3 (Frame rule (II)) *Assume that $G \vdash \{T_1^\#\} C \{T_2^\#\}$ and that $lhs(T_0^\#) \cap (modified(C) \cup \{\perp\}) = \emptyset$. Then $G \vdash \{T_1^\# \cup T_0^\#\} C \{T_2^\# \cup T_0^\#\}$.*

PROOF. Using Theorems 8.1 and 7.4 we get

$$sp(G, C, T_1^\# \cup T_0^\#) \supseteq sp(G, C, T_1^\#) \cup T_0^\# \supseteq T_2^\# \cup T_0^\#.$$

Since by Theorem 7.3 we have $G \vdash \{T_1^\# \cup T_0^\#\} C \{sp(G, C, T_1^\# \cup T_0^\#)\}$, the result follows by [Sub].

Example 8.4 Assume that

$$G \vdash \{T_1^\#\} C_1 \{T_3^\#\} \text{ and } G \vdash \{T_2^\#\} C_2 \{T_4^\#\}.$$

Further assume that $lhs(T_2^\#) \cap (modified(C_1) \cup \{\perp\}) = \emptyset$ and that $lhs(T_3^\#) \cap (modified(C_2) \cup \{\perp\}) = \emptyset$. Then Corollary 8.3 yields

$$G \vdash \{T_1^\# \cup T_2^\#\} C_1 \{T_3^\# \cup T_2^\#\} \text{ and } G \vdash \{T_3^\# \cup T_2^\#\} C_2 \{T_3^\# \cup T_4^\#\}$$

and therefore $G \vdash \{T_1^\# \cup T_2^\#\} C_1 ; C_2 \{T_3^\# \cup T_4^\#\}$.

A traditional view of modularity in the security literature is the “hook-up property” [20]: if two programs are secure then their composition is secure as well. Our logic satisfies the hook-up property for sequential composition; in our context, a secure program is one which has $[l \times h]$ as an invariant (if $[l \times h]$ is in the precondition, it is also in the strongest postcondition). With this interpretation, Sabelfeld and Sands’s hook-up theorem holds [29, Theorem 5].

9 The Smith-Volpano Security Type System

In the Smith-Volpano type system [31], variables are labelled by security types; for example, $x : (T, \kappa)$ means that x has type T and security level κ . The security typing rules are given in Fig. 6. To handle implicit flows due to conditionals, the technical development requires commands to be typed (**com** κ) with the intention that all variables assigned to in such commands have level

$$\begin{array}{c}
\frac{\Gamma, x : (\top, \kappa) \vdash E : (\top, \kappa)}{\Gamma, x : (\top, \kappa) \vdash x := E : (\mathbf{com} \ \kappa)} \\
\\
\frac{\Gamma \vdash E : (\mathbf{int}, \kappa) \quad \Gamma \vdash C_1 : (\mathbf{com} \ \kappa) \quad \Gamma \vdash C_2 : (\mathbf{com} \ \kappa)}{\Gamma \vdash \mathbf{if} \ E \ \mathbf{then} \ C_1 \ \mathbf{else} \ C_2 : (\mathbf{com} \ \kappa)} \\
\\
\frac{\Gamma \vdash C_1 : (\mathbf{com} \ \kappa) \quad \Gamma \vdash C_2 : (\mathbf{com} \ \kappa)}{\Gamma \vdash C_1 ; C_2 : (\mathbf{com} \ \kappa)} \\
\\
\frac{\Gamma \vdash E : (\mathbf{int}, \kappa) \quad \Gamma \vdash C : (\mathbf{com} \ \kappa)}{\Gamma \vdash \mathbf{while} \ E \ \mathbf{do} \ C : (\mathbf{com} \ \kappa)} \qquad \frac{\Gamma \vdash C : (\mathbf{com} \ \kappa_1) \quad \kappa \leq \kappa_1}{\Gamma \vdash C : (\mathbf{com} \ \kappa)}
\end{array}$$

Fig. 6. The Smith-Volpano Type System: Rules for Commands

at least κ . The judgement $\Gamma \vdash C : (\mathbf{com} \ \kappa)$ says that in the security type context Γ that binds free variables in C to security types, command C has type $(\mathbf{com} \ \kappa)$.

We now show a conservative extension: if a program is well-typed in the Smith-Volpano system, then for any two traces, the current values of low variables are independent of the initial values of high variables. For simplicity, we consider a program with only two variables, h with level H and l with level L .

Theorem 9.1 *Assume that C can be given a security type wrt. the environment $h : (_, H), l : (_, L)$. Then for all $T^\#$, if $[l \times h] \in T^\#$ then $[l \times h] \in sp(\emptyset, C, T^\#)$.*

The upshot of the theorem is that a well-typed program has $[l \times h]$ as *invariant*: if $[l \times h]$ appears in the precondition, then it also appears in the strongest postcondition.

The theorem is a straightforward consequence of the following lemma which facilitates a proof by induction. For L commands, the assumption $h \notin G$ in the lemma says that L commands cannot be control dependent on H guards.

Lemma 9.2 (1) *Suppose $h : (_, H), l : (_, L) \vdash C : (\mathbf{com} \ H)$. Then for all $G, T^\#$, if $[l \times h] \in T^\#$ then $[l \times h] \in sp(G, C, T^\#)$.*
(2) *Suppose $h : (_, H), l : (_, L) \vdash C : (\mathbf{com} \ L)$. Then for all $G, T^\#$, if $[l \times h] \in T^\#$ and $h \notin G$ then $[l \times h] \in sp(G, C, T^\#)$.*

PROOF. We prove the two parts of the lemma in turn. In both cases we go

by induction on the derivation of C (in the system of Fig. 6), with cases on the last rule used.

(Part 1.)

$C = z := E$. Clearly, $z \neq l$ as otherwise the assignment cannot be typed. By definition,

$$sp(G, z := E, T^\#) \supseteq \{[u \times w] \mid u \neq z \wedge [u \times w] \in T^\#\} \ni [l \times h].$$

$C = C_1 ; C_2$. We have $h : (_, H), l : (_, L) \vdash C_1 : (\mathbf{com} H)$ and $h : (_, H), l : (_, L) \vdash C_2 : (\mathbf{com} H)$. Inductively, we get

$$[l \times h] \in sp(G, C_1, T^\#) \text{ and then } [l \times h] \in sp(G, C_2, sp(G, C_1, T^\#)).$$

Thus $[l \times h] \in sp(G, C_1 ; C_2, T^\#)$.

$C = \mathbf{if} E \mathbf{then} C_1 \mathbf{else} C_2$. Then $h : (_, H), l : (_, L) \vdash C_1 : (\mathbf{com} H)$ and $h : (_, H), l : (_, L) \vdash C_2 : (\mathbf{com} H)$. Assume $[l \times h] \in T^\#$. Inductively,

$$[l \times h] \in sp(G_0, C_1, T^\#) \text{ and } [l \times h] \in sp(G_0, C_2, T^\#)$$

where $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T^\#\}$. Thus $[l \times h] \in sp(G_0, C_1, T^\#) \cap sp(G_0, C_2, T^\#)$, and we are done.

$C = \mathbf{while} E \mathbf{do} C_0$. Then $h : (_, H), l : (_, L) \vdash C_0 : (\mathbf{com} H)$. Let $T_0^\# = sp(G, C, T^\#)$. Then $T_0^\# = \text{lfp}(\mathcal{H}_C^{T^\#, G})$. Hence $T_0^\# = \mathcal{H}_C^{T^\#, G}(T_0^\#)$.

Let $T_1^\# = T_0^\# \cup [l \times h]$. Now

$$\mathcal{H}_C^{T^\#, G}(T_1^\#) = (sp(G_0, C_0, T_1^\#) \cap T^\#) \setminus \{[\perp \times w] \mid w \in G_0\}$$

where $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T_1^\#\}$. Inductively, as $[l \times h] \in T_1^\#$, we get $[l \times h] \in sp(G_0, C_0, T_1^\#)$. Thus,

$$[l \times h] \in \mathcal{H}_C^{T^\#, G}(T_1^\#). \tag{1}$$

Because $\mathcal{H}_C^{T^\#, G}$ is a monotone function, from $T_1^\# \preceq T_0^\#$ we get $\mathcal{H}_C^{T^\#, G}(T_1^\#) \preceq \mathcal{H}_C^{T^\#, G}(T_0^\#) = T_0^\#$. Thus

$$T_0^\# \subseteq \mathcal{H}_C^{T^\#, G}(T_1^\#). \tag{2}$$

Combining (1) and (2), we get $T_1^\# \subseteq \mathcal{H}_C^{T^\#, G}(T_1^\#)$, i.e., $\mathcal{H}_C^{T^\#, G}(T_1^\#) \preceq T_1^\#$. This shows that $\mathcal{H}_C^{T^\#, G}$ is reductive at $T_1^\#$, so by Tarski's theorem, $T_0^\# = \text{lfp}(\mathcal{H}_C^{T^\#, G}) \preceq T_1^\#$, that is, $T_1^\# \subseteq T_0^\#$. Hence $[l \times h] \in T_0^\#$.

Subtyping. For the subtyping rule, the result trivially follows by induction on the smaller derivation tree for C .

This completes Part 1 of the proof.

(Part 2.)

Subtyping. Assume $[l \times h] \in T^\#$ and $h \notin G$. By the typing rule, $h : (-, H), l : (-, L) \vdash C : (\mathbf{com} \ L)$ follows because $h : (-, H), l : (-, L) \vdash C : (\mathbf{com} \ \kappa_1)$ for some κ_1 . If $\kappa_1 = L$, the result follows inductively. If $\kappa_1 = H$, then we can apply Part 1 of the theorem to conclude that $[l \times h] \in sp(G, C, T^\#)$ holds for any G ; in particular, it must therefore hold for the G where $h \notin G$.

$C = z := E$. Assume $[l \times h] \in T^\#$ and $h \notin G$. By typing, $z : (-, L)$ and $h \notin \text{fv}(E)$, as otherwise the assignment cannot be typed. Hence $z = l$. By definition,⁵

$$sp(G, z := E, T^\#) \supseteq \{[z \times w] \mid w \notin G \wedge \forall u \in \text{fv}(E) \bullet [u \times w] \in T^\#\} \ni [l \times h].$$

$C = C_1 ; C_2$. Easy induction.

$C = \mathbf{if} \ E \ \mathbf{then} \ C_1 \ \mathbf{else} \ C_2$. By typing, $h : (-, H), l : (-, L) \vdash E : (\mathbf{int}, L)$. Hence $h \notin \text{fv}(E)$ and thus $h \notin G_0$, where $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T^\#\}$. Then inductively, we obtain $[l \times h] \in sp(G_0, C_1, T^\#)$ and $[l \times h] \in sp(G_0, C_2, T^\#)$. Hence $[l \times h] \in sp(G_0, C_1, T^\#) \cap sp(G_0, C_2, T^\#) = sp(G, C, T^\#)$.

$C = \mathbf{while} \ E \ \mathbf{do} \ C_0$. By typing, $h : (-, H), l : (-, L) \vdash E : (\mathbf{int}, L)$. Hence $h \notin \text{fv}(E)$. Now the proof proceeds similarly to the corresponding case in Part 1 and is omitted, except that we note that to use the induction hypothesis on the derivation of C_0 , we need to show $h \notin G_0$, where $G_0 = G \cup \{w \mid \exists x \in \text{fv}(E) \bullet [x \times w] \notin T_1^\#\}$. Since $h \notin G$, it is sufficient to show that for all $x \in \text{fv}(E)$ it holds that $[x \times h] \in T_1^\#$. But this follows since $h \notin \text{fv}(E)$ and $[l \times h] \in T_1^\#$.

⁵ In case there are several low variables, we would use the argument:

$$sp(G, z := E, T^\#) \supseteq \{[u \times w] \mid u \neq z \wedge [u \times w] \in T^\#\} \ni [l \times h]$$

to deal with low variables not assigned to.

10 Discussion

Perspective. This paper specifies an information flow analysis for confidentiality using a Hoare-like logic and shows an application of the analysis to a program transformation, namely, slicing. The concrete pre- and post-traces of a program are abstracted by independences. Independences can be statically checked against the logic and can be statically computed using strongest post-conditions. We also show how the notion of independences underlies a classic type-based information flow analysis due to Smith and Volpano [31].

Giacobazzi and Mastroeni [14] consider attackers as abstract interpretations and generalize the notion of noninterference by parameterizing it wrt. what an attacker can analyze about the input/output information flow. For instance, assume an attacker can only analyze the *parity* (odd/even) of values. Then

```
while h do l := l + 2 ; h := h - 1
```

is secure, although it contains an update of a low variable under a high guard. We might try to model this approach in our framework by parameterizing Definition 3.1 wrt. *parity*, but it is not clear how to alter the proof rules accordingly. Instead, we envision our logic to be put on top of abstract interpretations. In the parity example, the above program would be abstracted to

```
while h do h := h - 1
```

which our logic already deems secure for an attacker not able to observe non-termination.

Related work. The most closely related work is that of Clark, Hankin, and Hunt [8], who consider a language similar to ours and then extend it to Idealized Algol, requiring distinguishing between identifiers and locations. The analysis for Idealized Algol is split in two stages: the first stage does a control-flow analysis, specified using a flow logic [21]. The second stage specifies what is an acceptable information flow analysis with respect to the control-flow analysis. The precision of the control-flow analysis influences the precision of the information flow analysis. Flow logics usually do not come with a frame rule so it is unclear what modularity properties their analysis satisfies. For each statement S in the program, they compute the set of dependences introduced by S ; a pair (x, y) is in that set if different values for y prior to execution of S may result in different values for x after execution of S . For a complete program, they thus, as expected, compute essentially the same information as we do, but the information computed *locally* is different from ours: we estimate

if different *initial* values of \mathbf{y} , i.e., values of \mathbf{y} prior to execution of *the whole program*, may result in different values for \mathbf{x} after execution of S .

Joshi and Leino [19] provide an elegant semantic characterization of noninterference that allows handling both nontermination-sensitive and nontermination-insensitive noninterference. Their notion of security for a command C is equationally characterized by $C ; HH = HH ; C ; HH$, where HH means that an arbitrary value is assigned to a high variable. They show how to express their notion of security in Dijkstra’s weakest precondition calculus. Although they do not consider synthesizing loop invariants, this can certainly be done via a fixpoint computation with weakest preconditions. However, their work is not concerned with computing dependences.

Darvas, Hähnle and Sands [12] use dynamic logic to express secure information flow in JavaCard. They discuss several ways that noninterference can be expressed in a program logic, one of which is as follows: consider a program with variables \mathbf{l} and \mathbf{h} . Consider another copy of the program with \mathbf{l} , \mathbf{h} relabeled to fresh variables \mathbf{l}' , \mathbf{h}' respectively. Then, noninterference holds in the following situation: running the original program and the copy sequentially such that the initial state satisfies $\mathbf{l} = \mathbf{l}'$ should yield a final state satisfying $\mathbf{l} = \mathbf{l}'$. They are also interested in showing insecurity, by exhibiting distinct initial values for high variables that give distinct current values of low variables; to achieve this accuracy, they need the power of a general purpose theorem prover, which is also helpful in that they can express declassification, as well as treat exceptions (which most approaches based on static analysis cannot easily be extended to deal with).

Barthe, D’Argenio and Rezk [6] use the same idea of self-composition (i.e., composing a program with a copy of itself) as Darvas et alii and investigate “abstract” noninterference [14] for several languages. By parameterizing noninterference with a property, they are able to handle more general information flow policies, including a form of declassification known as delimited information release [27]. They show how self-composition can be formulated in logics describing these languages, namely, Hoare logic, separation logic, linear temporal logic, etc. They also discuss how to use their results for model checking programs with finite state spaces to check satisfaction of their generalized definition of noninterference.

The first work that used a Hoare-style semantics to reason about information flow was by Andrews and Reitman [4]. Their assertions keep track of the security level of variables, and are able to deal even with parallel programs. However, no formal correctness result is stated.

Conclusion. The work reported in this paper was inspired in part by presentations by Roberto Giacobazzi and Reiner Hähnle at the Dagstuhl Seminar on Language-based Security (October 2003). The reported work is only the first step in our goal to formulate more general definitions of noninterference in terms of program independence, such that the definitions support modular reasoning. We are in the process of extending the framework in this paper to handle a richer language, with methods, pointers, objects and dynamic memory allocation. An obvious goal is interprocedural reasoning about variable and field independences, perhaps using a higher-order version of the frame rule [23]. We would also like to explore, via abstract interpretation and perhaps following Schmidt’s development [30], whether our Hoare-like logic is the “best” possible one with respect to the underlying abstract interpretation.

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References

- [1] Martín Abadi, Anindya Banerjee, Nevin Heintze, and Jon G. Riecke. A core calculus of dependency. In *ACM Symposium on Principles of Programming Languages (POPL)*, pages 147–160, 1999.
- [2] Torben Amtoft and Anindya Banerjee. Information flow analysis in logical form. In *Static Analysis Symposium (SAS)*, volume 3148 of *Lecture Notes in Computer Science*, pages 100–115. Springer-Verlag, 2004.
- [3] Torben Amtoft and Anindya Banerjee. Information flow analysis in logical form. Technical Report CIS TR 2004-3, Kansas State University, April 2004.
- [4] G. R. Andrews and R. P. Reitman. An axiomatic approach to information flow in programs. *ACM Transactions on Programming Languages and Systems*, 2(1):56–75, January 1980.
- [5] Anindya Banerjee and David A. Naumann. Secure information flow and pointer confinement in a Java-like language. In *IEEE Computer Security Foundations Workshop (CSFW)*, pages 253–270. IEEE Computer Society Press, 2002.

- [19] Rajeev Joshi and K. Rustan M. Leino. A semantic approach to secure information flow. *Science of Computer Programming*, 37:113–138, 2000.
- [20] Daryl McCullough. Specifications for multi-level security and a hook-up. In *IEEE Symposium on Security and Privacy*, pages 161–166, 1987.
- [21] Flemming Nielson, Hanne Riis Nielson, and Chris Hankin. *Principles of Program Analysis*. Springer-Verlag, 1999. Web page at www.imm.dtu.dk/~riis/PPA/ppa.html.
- [22] Peter O’Hearn, John Reynolds, and Hongseok Yang. Local reasoning about programs that alter data structures. In *Computer Science Logic*, volume 2142 of *Lecture Notes in Computer Science*, pages 1–19. Springer-Verlag, 2001.
- [23] Peter O’Hearn, Hongseok Yang, and John Reynolds. Separation and information hiding. In *ACM Symposium on Principles of Programming Languages (POPL)*, pages 268–280, 2004.
- [24] Peter Ørbæk and Jens Palsberg. Trust in the λ -calculus. *Journal of Functional Programming*, 7(6):557–591, November 1997.
- [25] François Pottier and Vincent Simonet. Information flow inference for ML. *ACM Transactions on Programming Languages and Systems*, 25(1):117–158, January 2003.
- [26] John C. Reynolds. Separation logic: a logic for shared mutable data structures. In *IEEE Symposium on Logic in Computer Science (LICS)*, pages 55–74. IEEE Computer Society Press, 2002.
- [27] Andrei Sabelfeld and Andrew Myers. A model for delimited information release. In *Proceedings of the International Symposium on Software Security (ISSS)*, number 3233 in *Lecture Notes in Computer Science*, pages 174–191. Springer-Verlag, 2004.
- [28] Andrei Sabelfeld and Andrew C. Myers. Language-based information-flow security. *IEEE J. Selected Areas in Communications*, 21(1):5–19, January 2003.
- [29] Andrei Sabelfeld and David Sands. A Per model of secure information flow in sequential programs. *Higher-order and Symbolic Computation*, 14(1):59–91, 2001.
- [30] David A. Schmidt. Structure-preserving binary relations for program abstraction. In *The Essence of Computation: Complexity, Analysis, Transformation – Essays dedicated to Neil D. Jones*, number 2566 in *Lecture Notes in Computer Science*. Springer-Verlag, 2002.
- [31] Dennis Volpano and Geoffrey Smith. A type-based approach to program security. In *Proceedings of TAPSOFT’97*, number 1214 in *Lecture Notes in Computer Science*, pages 607–621. Springer-Verlag, 1997.